

SPIE 2011 AL EUVL Conference

EUVL Technology Status Summary

March 3, 2011

EUV Litho, Inc.

www.euvlitho.com



β level EUVL Scanner are Shipping to Customers and Operating in Field



β level EUVL Scanner Status and Improvements Over Last One Year

Progress since last SPIE from modules to system shipment

- SPIE 2010 report was prior to 1st system completion
- NXE:3100 module verification
 - Lens
 - Handlers
 - Stages
 - Source
- 24nm imaging from ADT
 - ~15mJ/cm²
- Today: 2 systems shipped to customers, 1st is operational
- NXE:3100 system verification
 - <5% System Flare
 - <7nm matched Overlay
 - Wafer Processing at 5wph
 - Upgrade path to 60wph Q4/11
- 22nm data from NXE:3100
 - 10mJ/cm²
- 18nm resolution shown

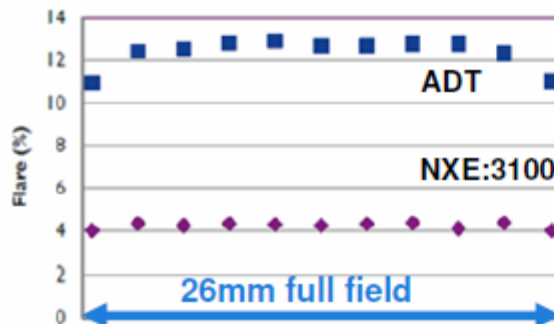
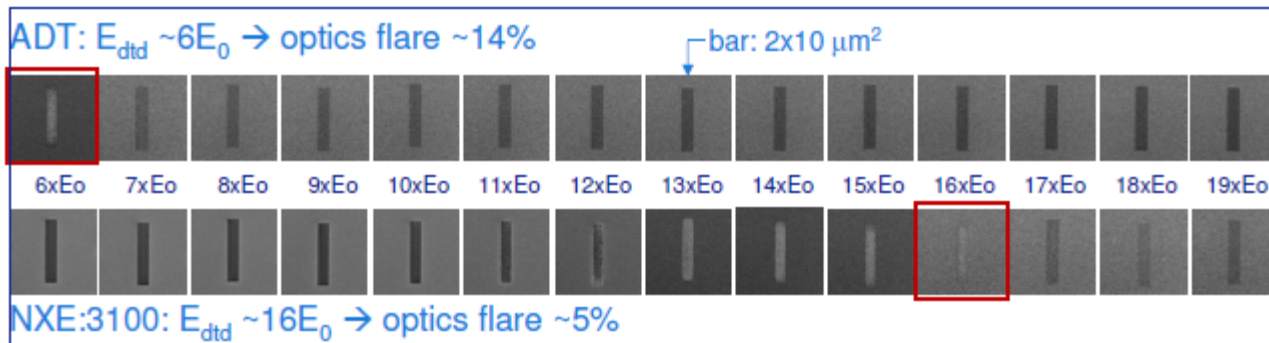
Slide 4 | SPIE March 2011



NXE:3100 Status

Flare < 5%

NXE:3100 Flare is < 5% under 2μm bar
confirms lens qualification at Zeiss



$$\text{flare} = \frac{E_0}{E_{dtd}} \times 100\% - \text{mask flare}$$

$$\text{mask flare} = \frac{R_{abs}}{R_{multi}} \times 100\%$$

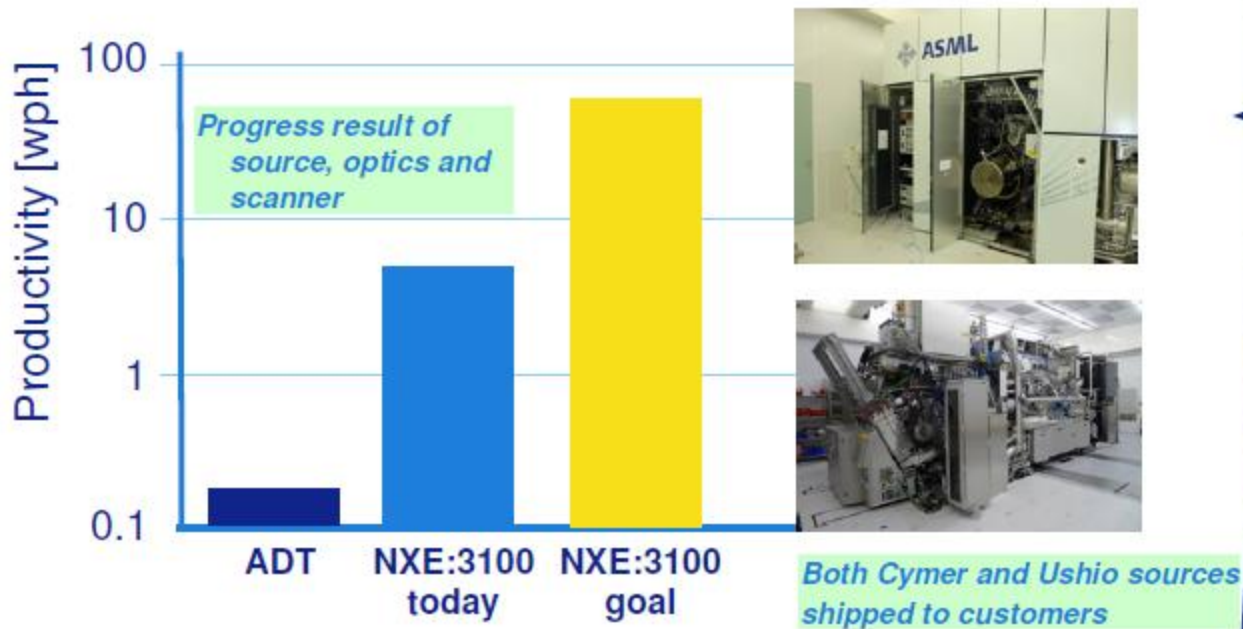


NXE:3100 Status

Throughput 20x Improved (over ADT)

NXE:3100 >20x productivity than ADT

10x short to meet 60wph, upgrade path in place



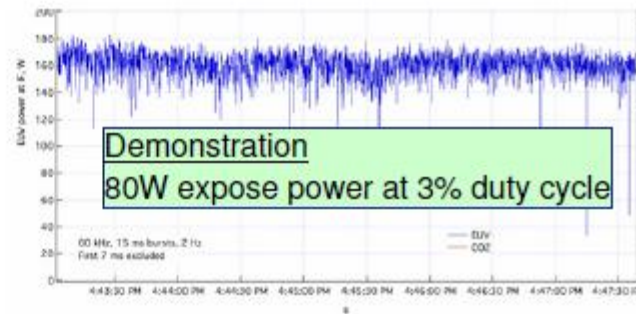
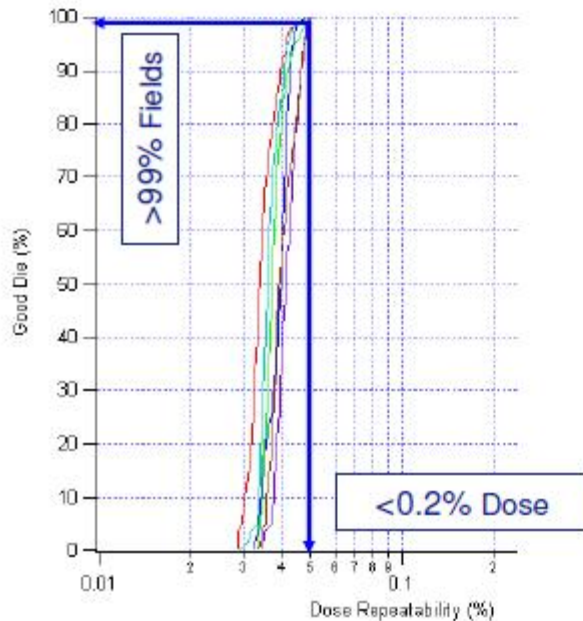
Reference conditions: 125 fields, 16x32mm, 10mJ/cm²

Slide 10 | SPIE March 2011



Cymer LPP Source: 11 W Exposure Power

4 Cymer LPP sources integrated to NXE:3100
11W expose power being implemented at scanner



CYMER

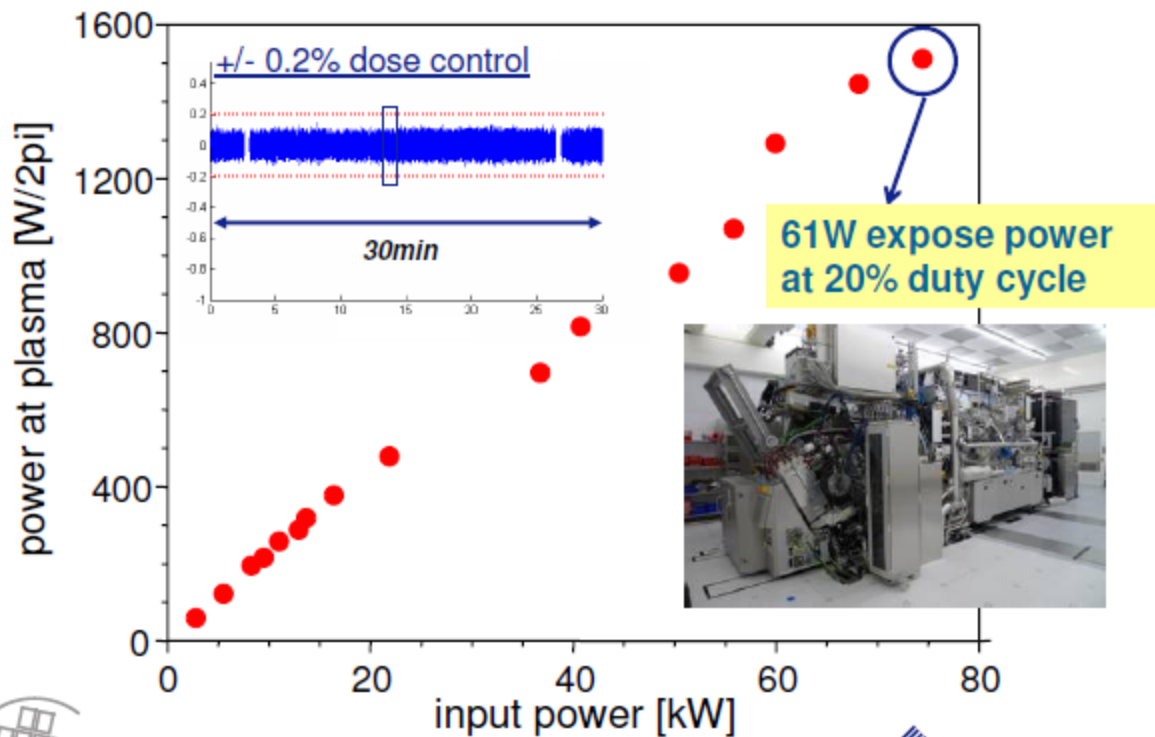
Slide 11 | SPIE March 2011

ASML

EUV
LITHO, INC.

Xtreme DPP Source: 12 W Exposure Power

1st Ushio source integrated and exposing wafers
9 shell collector, 7W integrated, 12W (61W, 20%) demonstrated

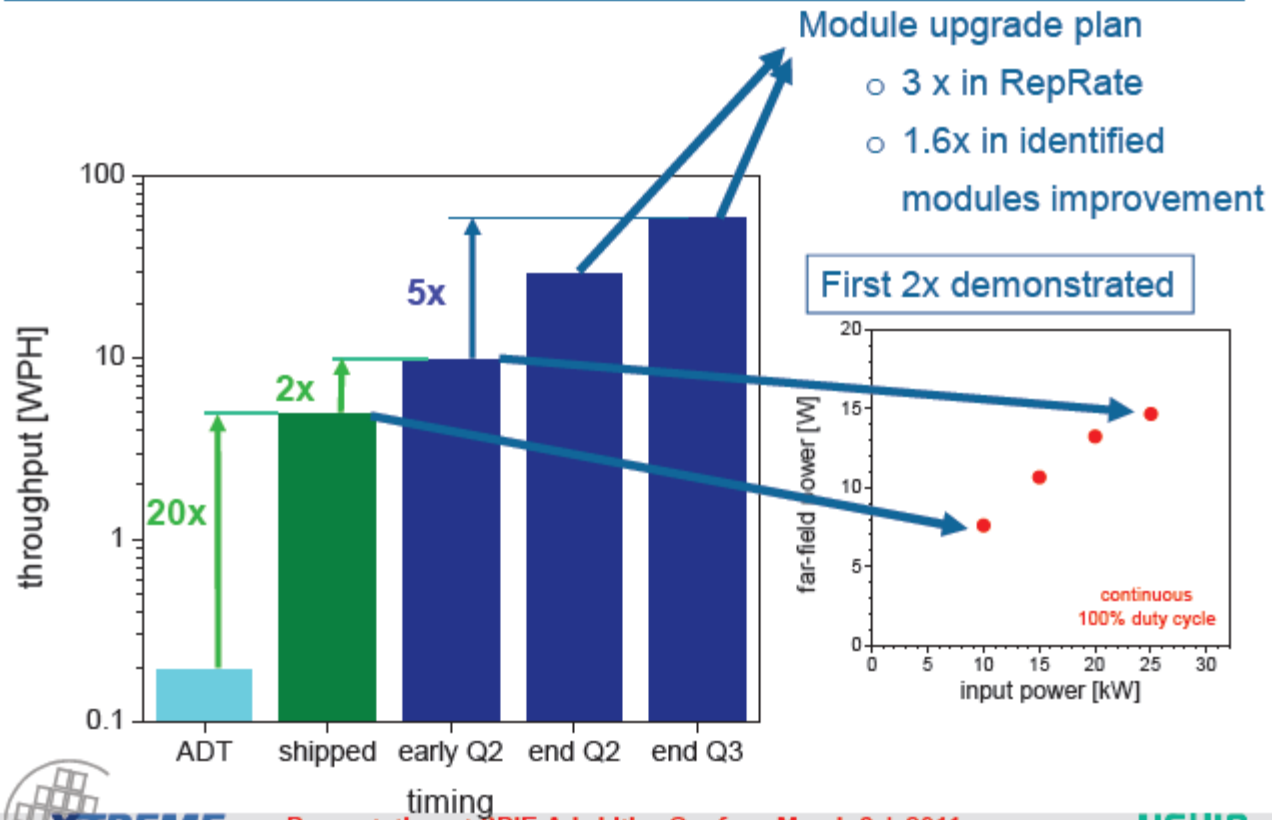


Slide 12 | SPIE March 2011



Xtreme DPP Source: Roadmap

NXE 3100 Field Upgrade Plan By Module Swaps



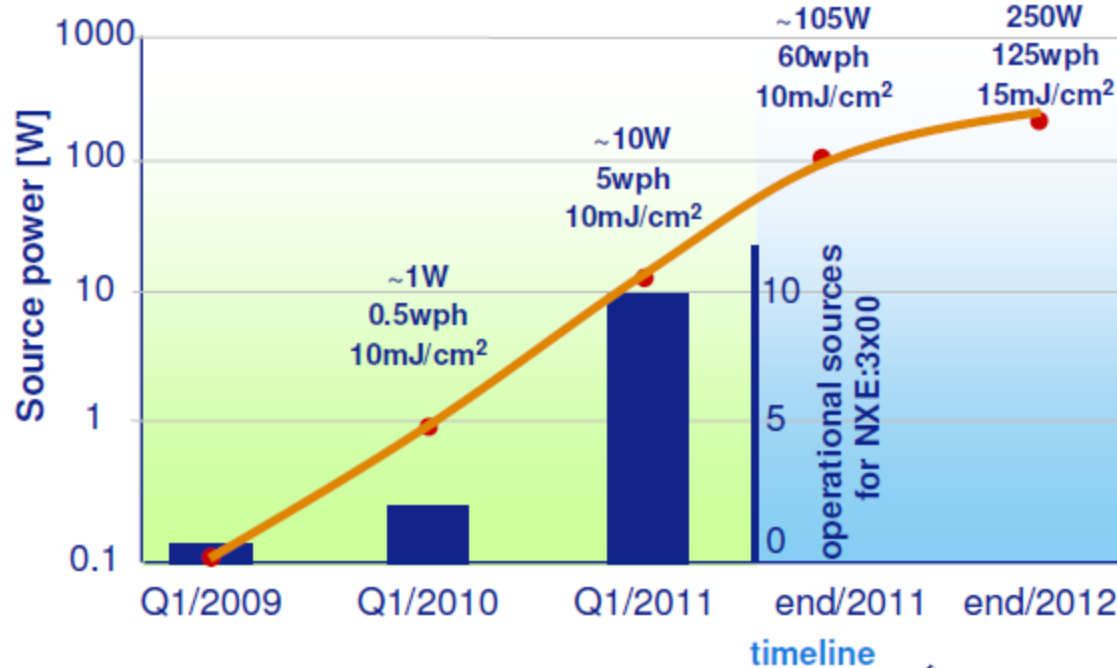
Presentation at SPIE Adv.Litho Conf. March 3rd, 2011



Source Power & Throughput Roadmap

Source industrialization is progressing

3 suppliers committed for volume manufacturing with NXE:3300B



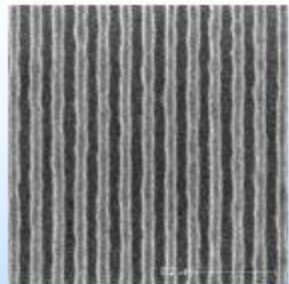

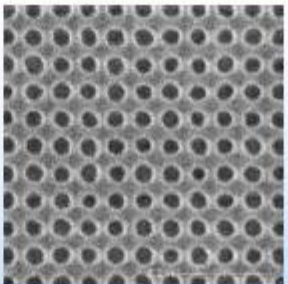
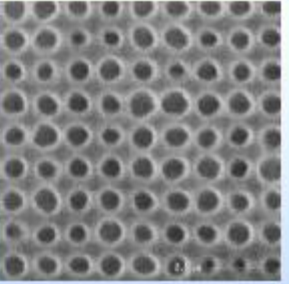
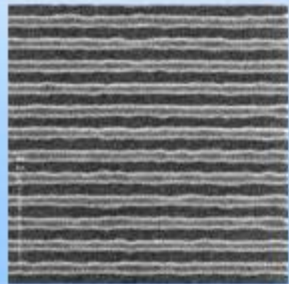
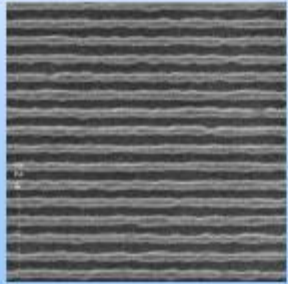
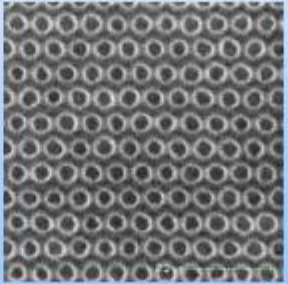

Slide 14 | SPIE March 2011



NXE:3100 Status

Resolution -24 nm L/S ; 26 nm CH

Resolution: L/S < 27nm and C/Hs < 30nm half pitch

27nm 1:1 L/S	24nm 1:1 L/S	30nm 1:1 C/H	26nm 1:1 C/H
			
			

*Conventional 0.8 sigma illumination
50nm SPUR-V002 on bare Si*

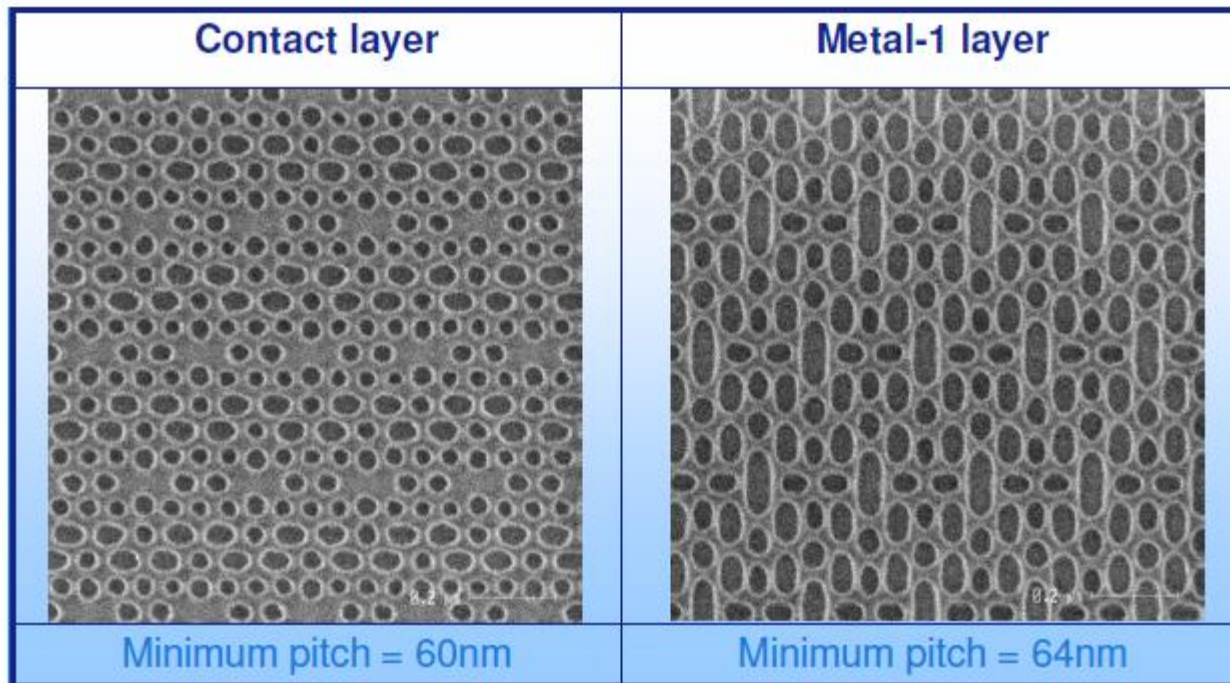
Slide 25 | SPIE March 2011



NXE:3100

Resolution- 14 nm node SRAM CH resolved

14nm node SRAM C/H and metal-1 layer well resolved
0.038 μ m² bit cell-size, hp 30/32nm, further OPC required



50nm SPUR-V002 on 20nm UL, TBAH develop + FRM Extreme rinse
Dose CH = 20.0 mJ/cm², dose M1 = 18 mJ/cm²

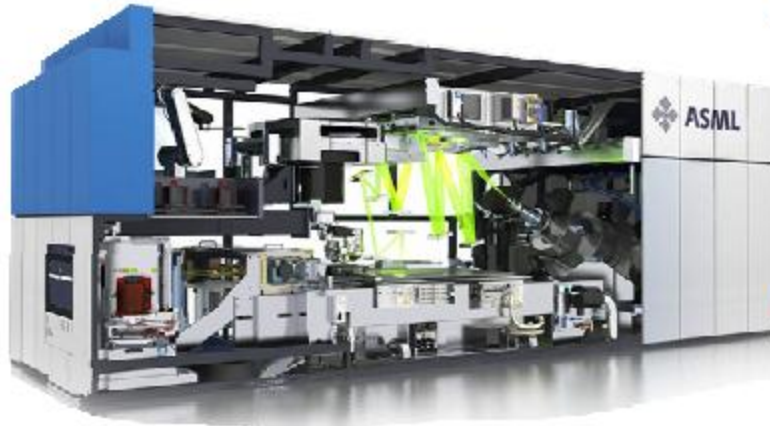
Slide 40 | SPIE March 2011



HVM Level EUVL Scanner Specifications

NXE:3300B 1st shipment: H1 2012

2nd generation of NXE platform, NA raised to 0.33NA



Specifications

- Imaging
 - NA = 0.33
 - $\sigma=0.2-0.9$ / OAI
 - Resolution 22 nm
18/16nm with OAI
- Overlay
 - DCO 3.0nm
 - MMO 5.0nm
- Productivity
 - 125 wph
 - 15 mJ/cm² resist



Slide 46 | SPIE March 2011

