

ASML

EUV Platform Readiness

Progress and Challenges

Klaus Schuegraf, Daniel Brown, Uwe Stamm & Christian Wagner

2014 International Workshop on EUV Lithography
Maui, Hawaii June 25, 2014

Agenda

- **EUV overview**
- Source power and availability technology development



EUV overview: Progress toward production insertion

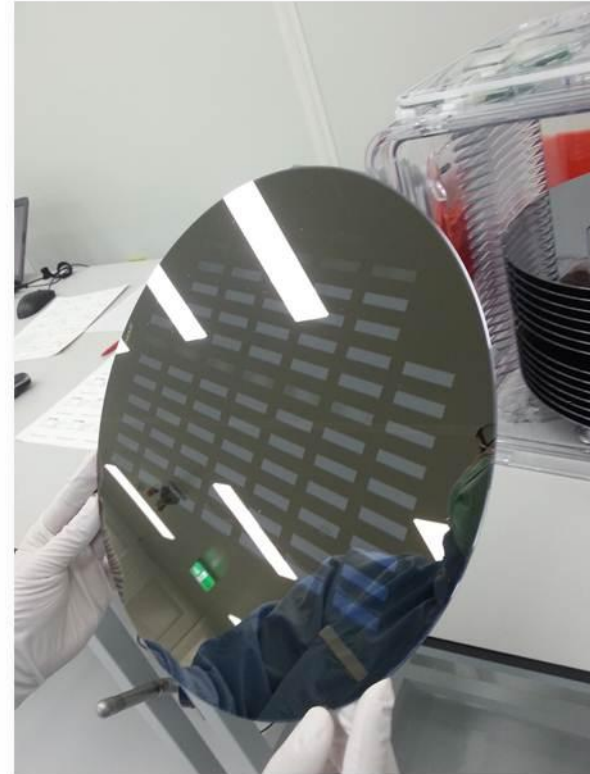
- Multiple customers are qualifying EUV for insertion at the 10 nm logic node
- For process development, customers typically require 100 wafers per day, increasing to 500 wafers per day on average for production qualification
- We have provided customers with that process development capability
- In 2016 we will provide our customers with the productivity needed for volume production



- 6 NXE:3300B systems fully qualified and shipped to customers
- 5 more NXE:3300B systems being integrated
- 4th generation NXE system (NXE:3350B) integration ongoing

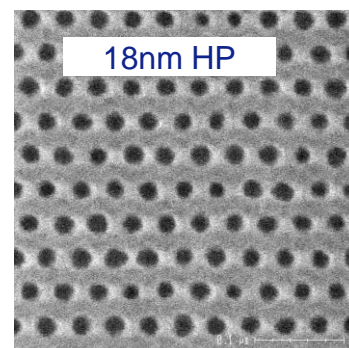
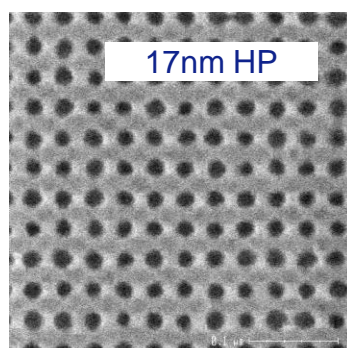
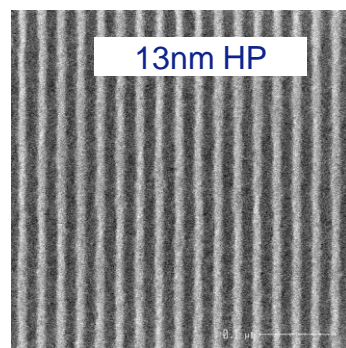
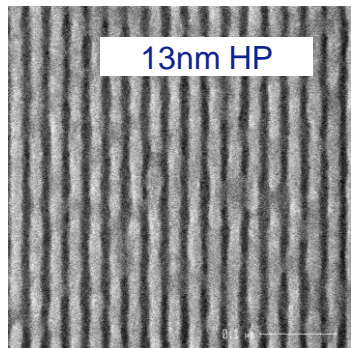
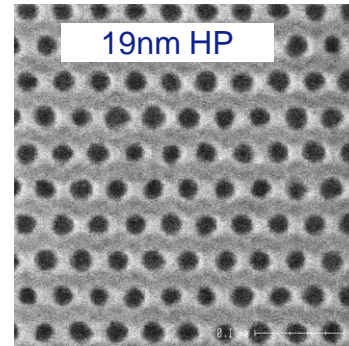
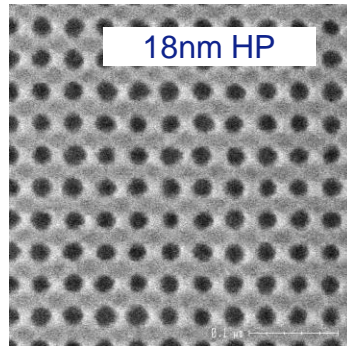
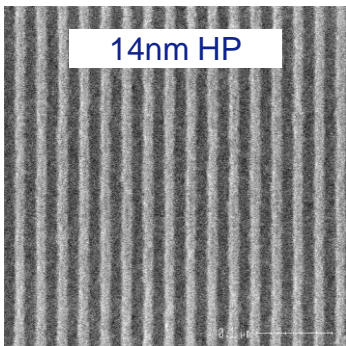
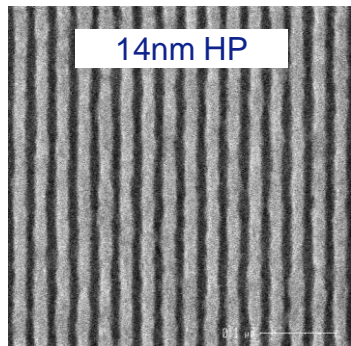
NXE:3300B systems are exposing at customer sites

The screenshot shows the 'Remote Operation - ASML.TWINSCAN.M6269_HPE301.RTO' interface. At the top, it displays system status: SECS Control: OFFLine, SECS Comm: Not communicating, Date: 12/11/2013, Time: 22:22:48, Screen Control: OIU, Machine: AT, Release: 7.2.0.b, User: ASML, and Level: SERVICE ENG. The 'Lot Data' section shows 'Wafers Remaining: 1 [wafers]', 'Accepted: 0 [wafers]', 'Rejected: 0 [wafers]', and 'Time Remaining: [minutes]' with a 0% progress bar. A 'Lot Definition' section includes buttons for 'New...', 'Modify...', 'Copy', 'Delete', and 'Report'. Below this is a 'Lot Queue' table with columns 'Lot Name', 'Status', and 'Acc/Rej'. The queue contains one entry: 'm6269_Sm1ley' with status 'PROCESSING' and '000/000'. The 'Wafer Monitor' section shows a reticle '626946329001' and a 'Wafer Carrier (FOUP) (Locked)' with a grid of 24 slots. The bottom status bar includes icons for various system components and a red warning triangle.



NXE:3300B resolution for single exposures

Dense line spaces, regular and staggered contact holes



Dipole30,

Dipole45,

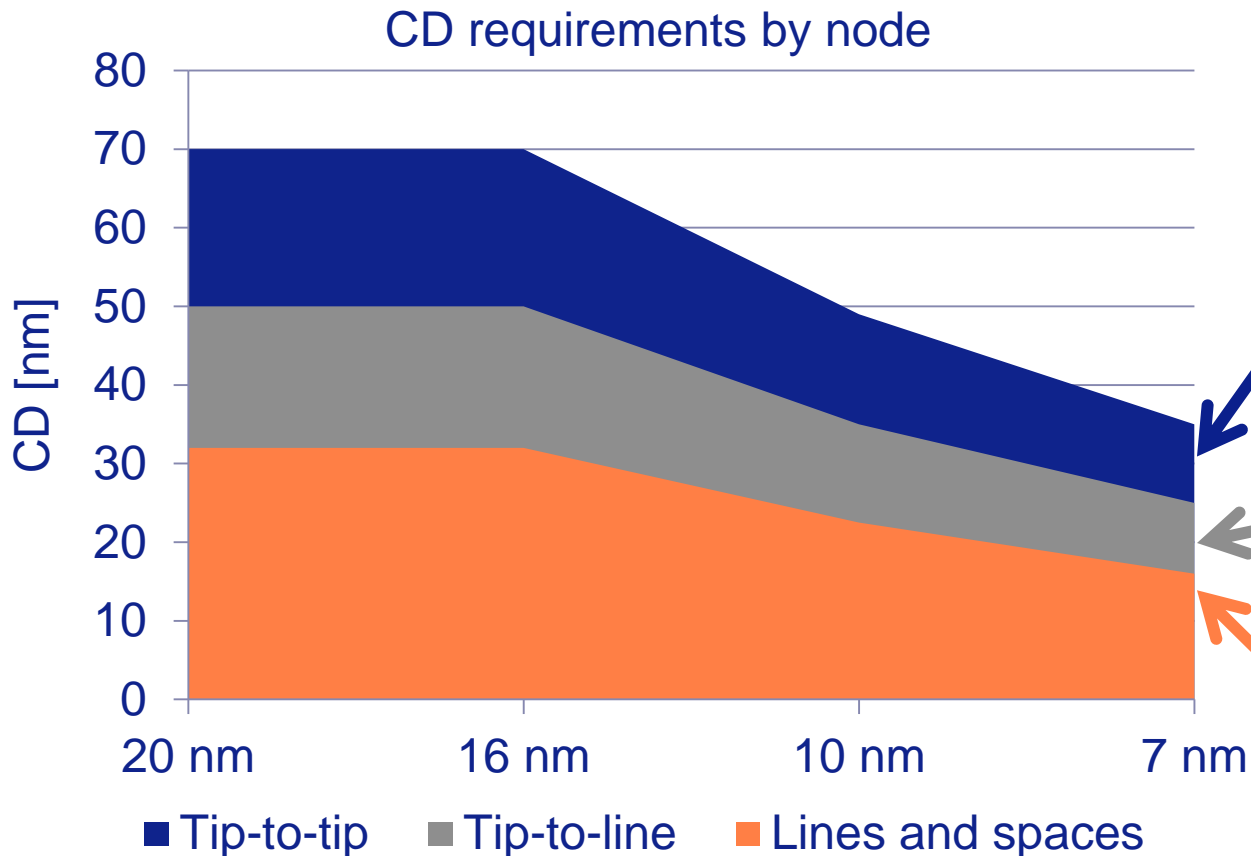
Quasar 30 (CAR)

Large Annular (CAR)

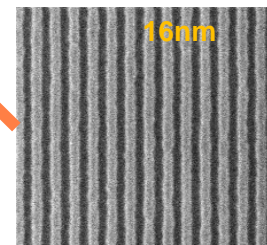
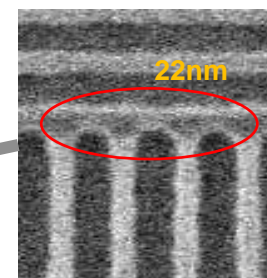
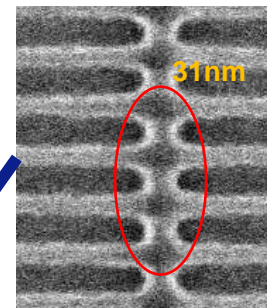
**Chemically Amplified Resist
(CAR)**

Inpria Resist

EUV meets aggressive 2D logic imaging requirements

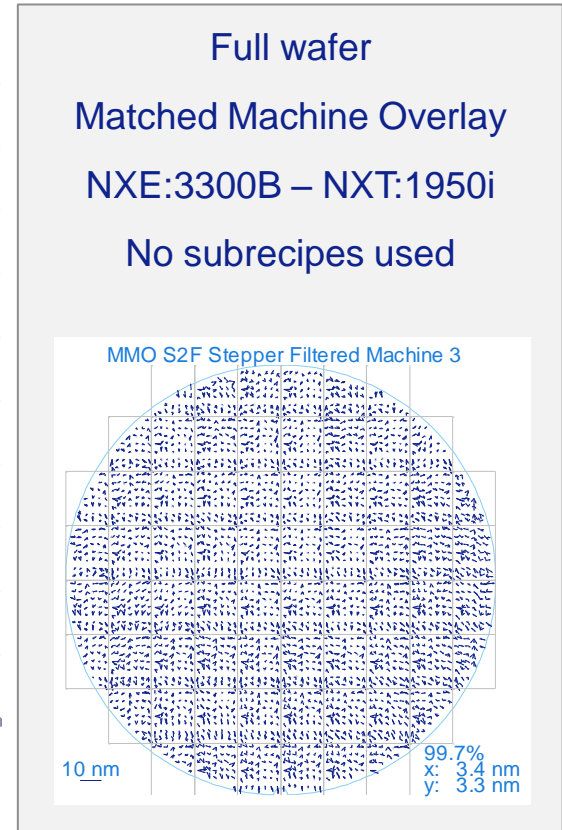
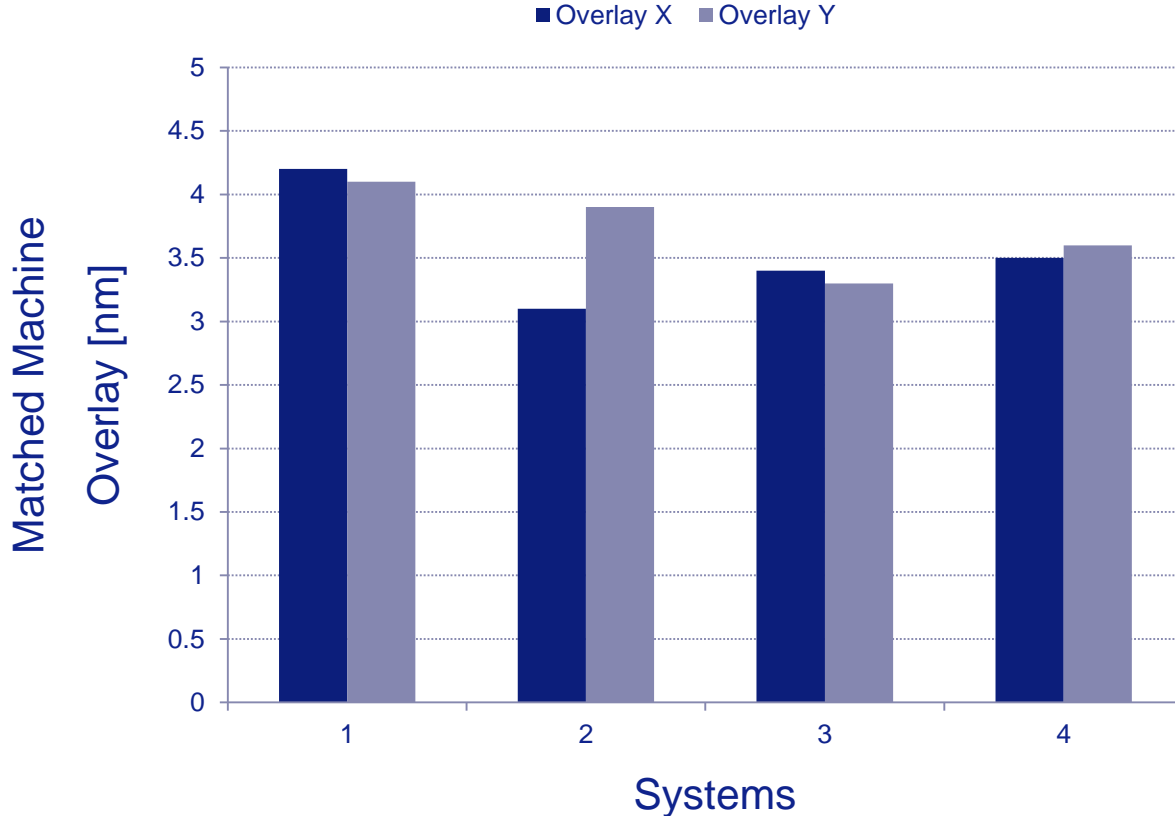


EUV (SE)*

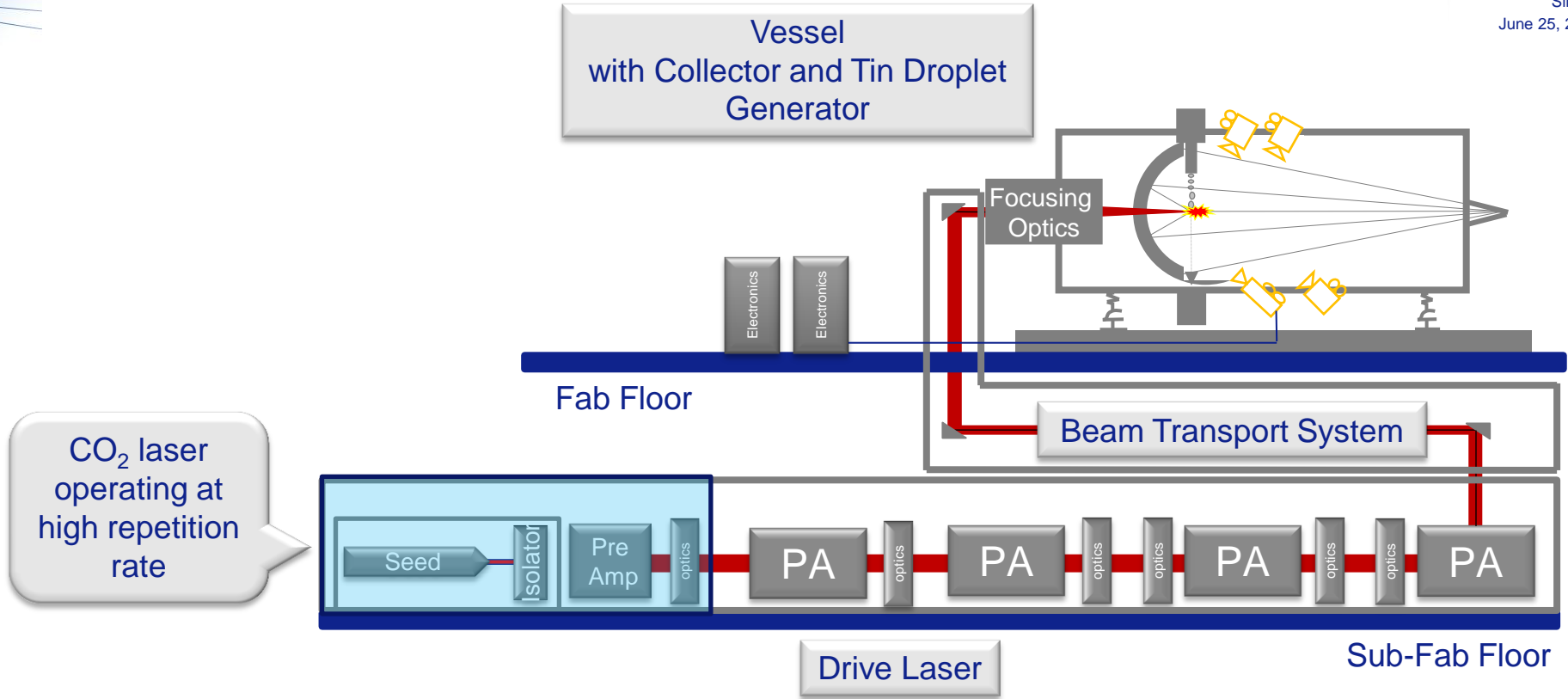


* Single Expose (SE) using high dose resist @ ~50mJ/cm²

Good matched machine overlay performance on multiple systems (NXE:3300B to immersion)



Laser Produced Plasma (LPP) source architecture



Ramping source production for NXE:3300B vessels

ASML

Confidential

Slide 9

June 25, 2014



Multiple development and integration sources supporting NXE:3300B source production



NXE:3300B
Drive Laser

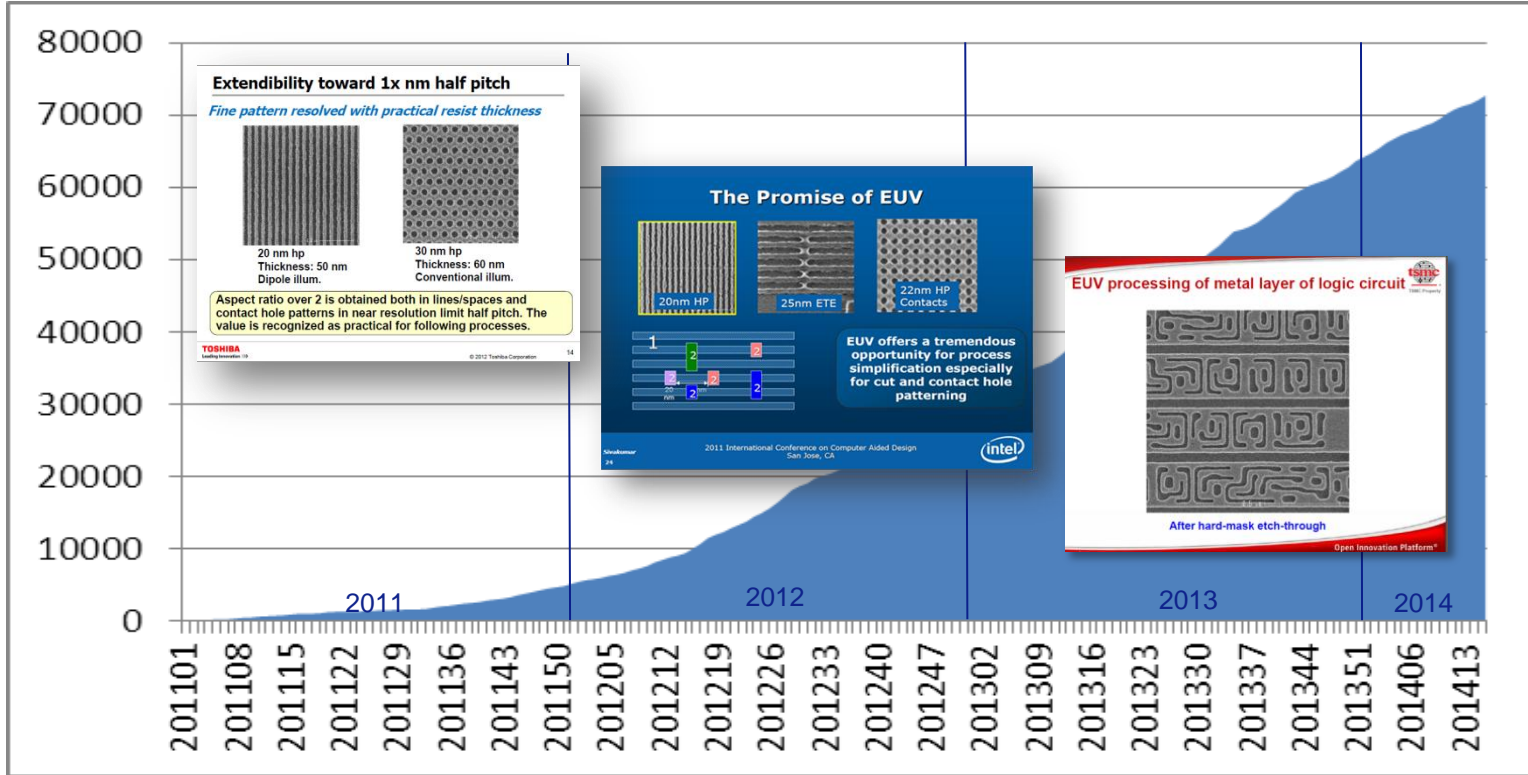


NXE:3300B
Source

NXE:3100 continues to be used for cycles of learning

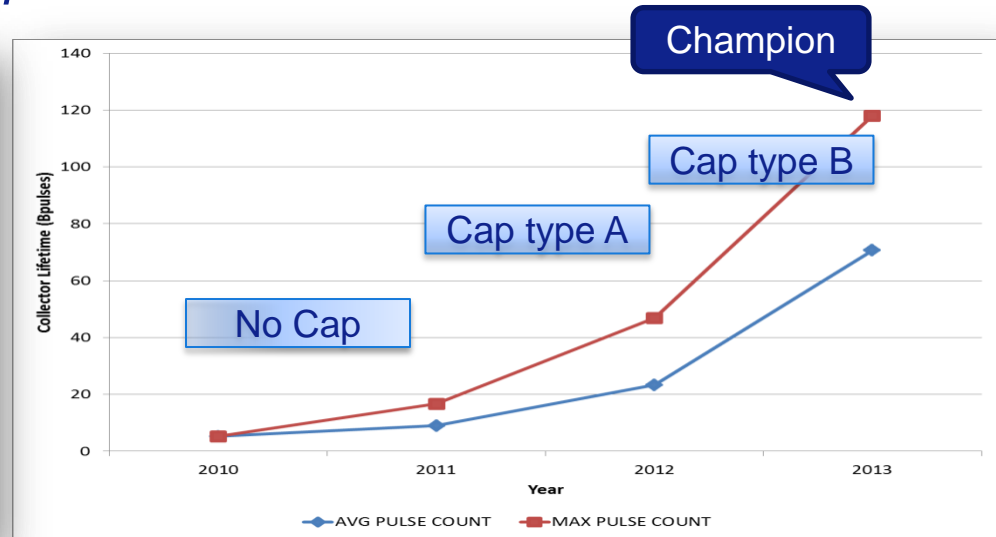
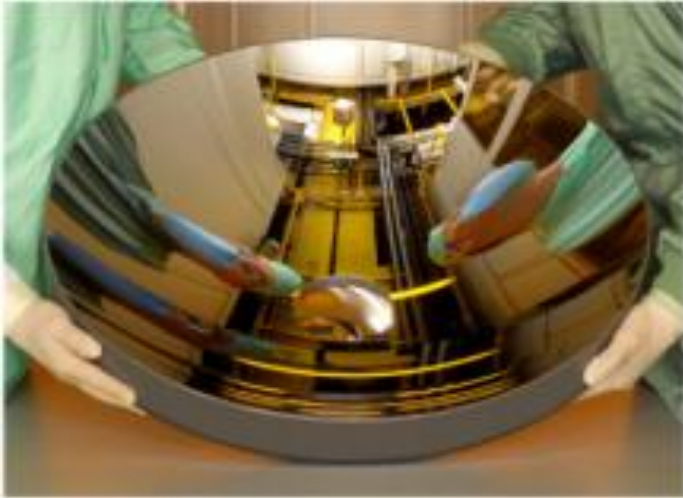
6 systems operational with source availability >70%

Cumulative wafers exposed on NXE:3100



NXE:3100: collector lifetime is key to high availability

Field collector performance improved with changes in capping layer technology and vacuum control

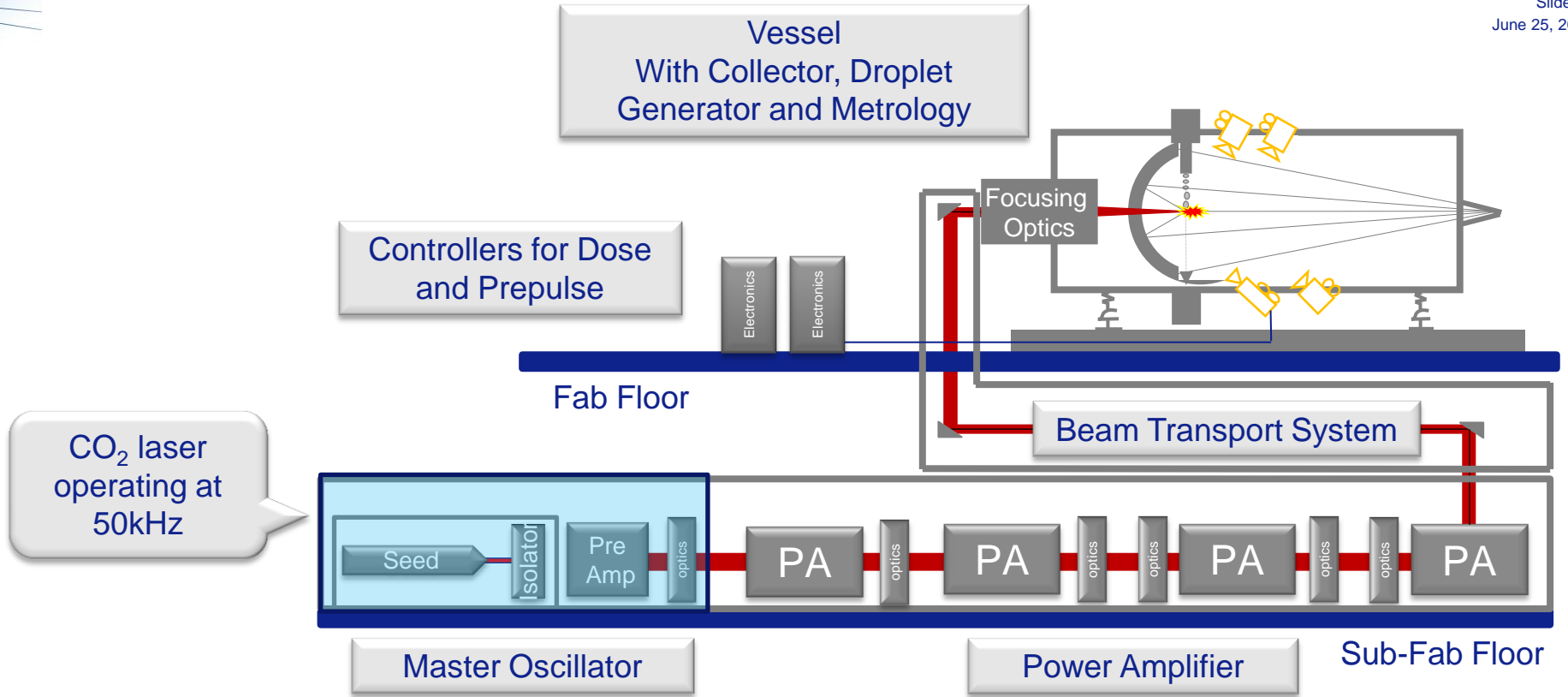


- 5steradian near-normal incidence graded multilayer coated collector
- Collector reflectivity maintained using hydrogen to prevent ion damage and tin vapor deposition
- 70 Gpulse average lifetime in 2013 for NXE:3100 field sources
- Initial usage ~10Gpulse per month
- Lifetime requirement >100Gpulse in high volume use

Agenda

- EUV overview
- **Source power and availability technology development**

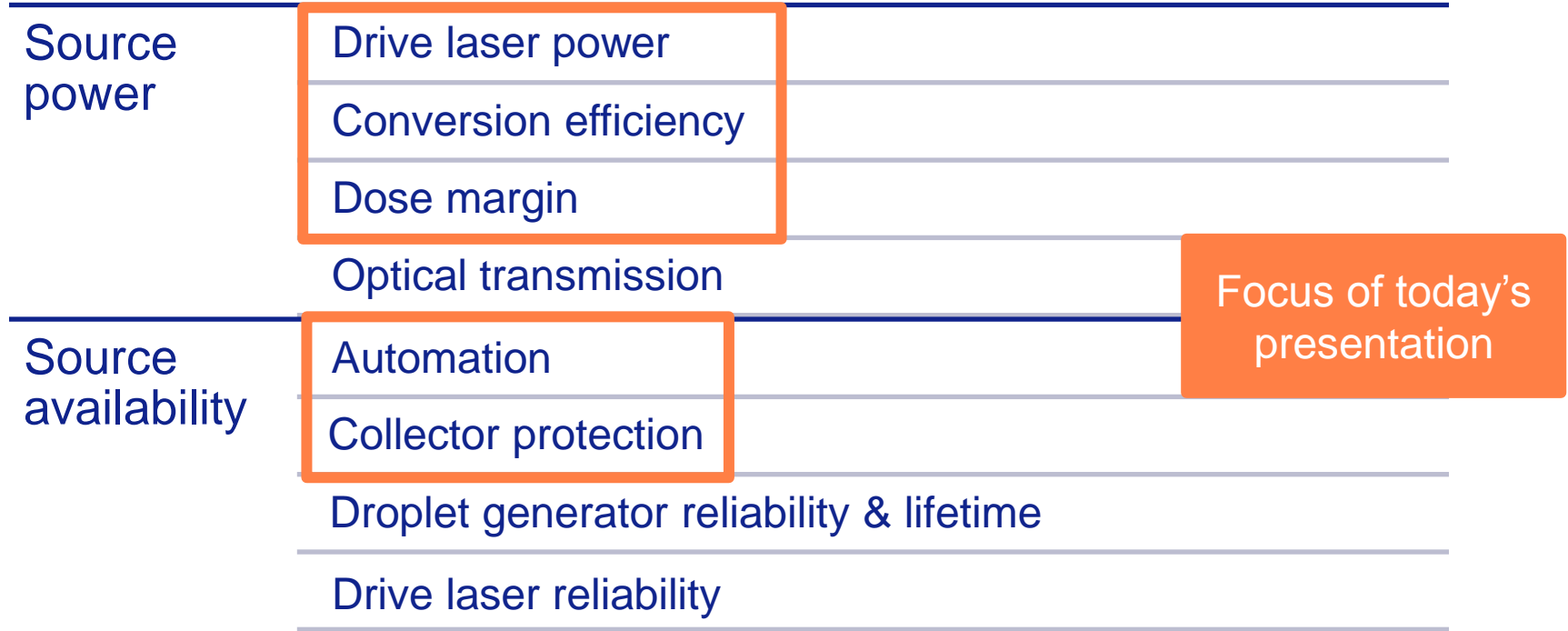
MOPA Prepulse LPP source architecture for NXE:3300B



MOPA - Master Oscillator Power Amplifier

Source power and availability drive productivity

Technology development work is ongoing to improve all aspects



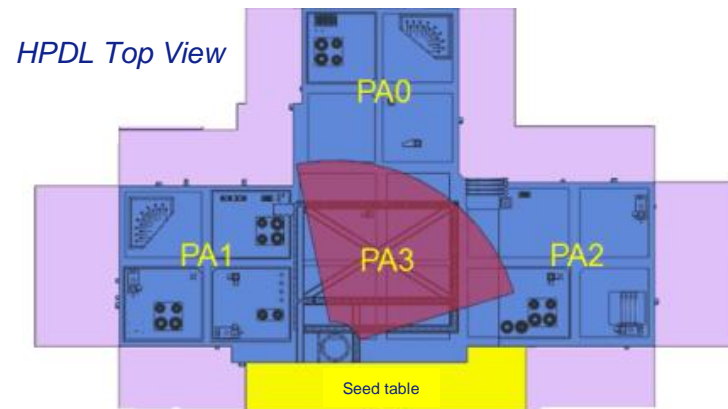
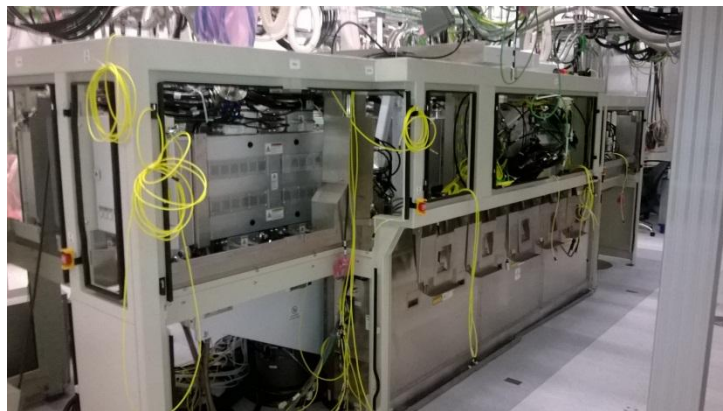
Source power scaling

Scaling parameter			
EUV Power in Burst (W)	40	80	250
Laser Power (kW)	20	26	43
Conversion Efficiency (%)	2.5	3.5	4.5
Dose margin (%)	35	20	10

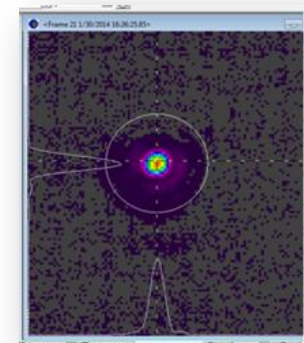
Key scaling parameters are:

- *Laser power*
- *Conversion efficiency*
- *Dose margin*

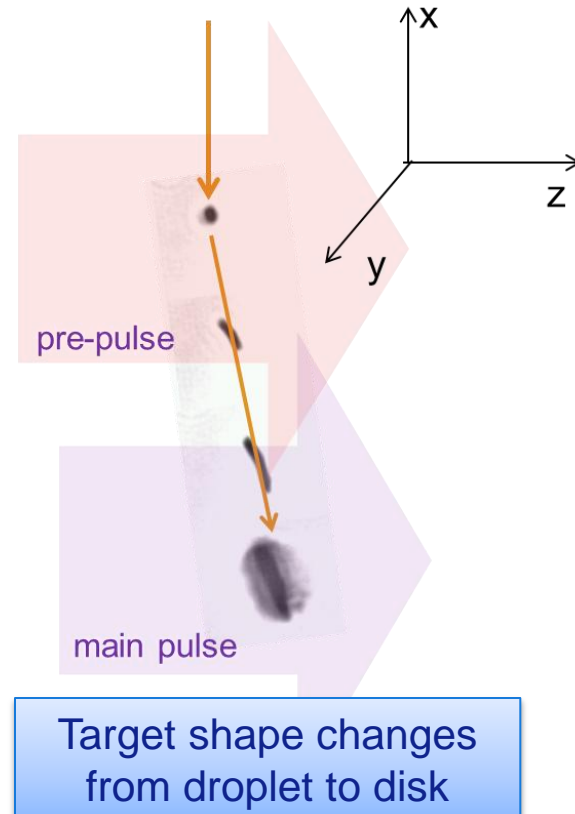
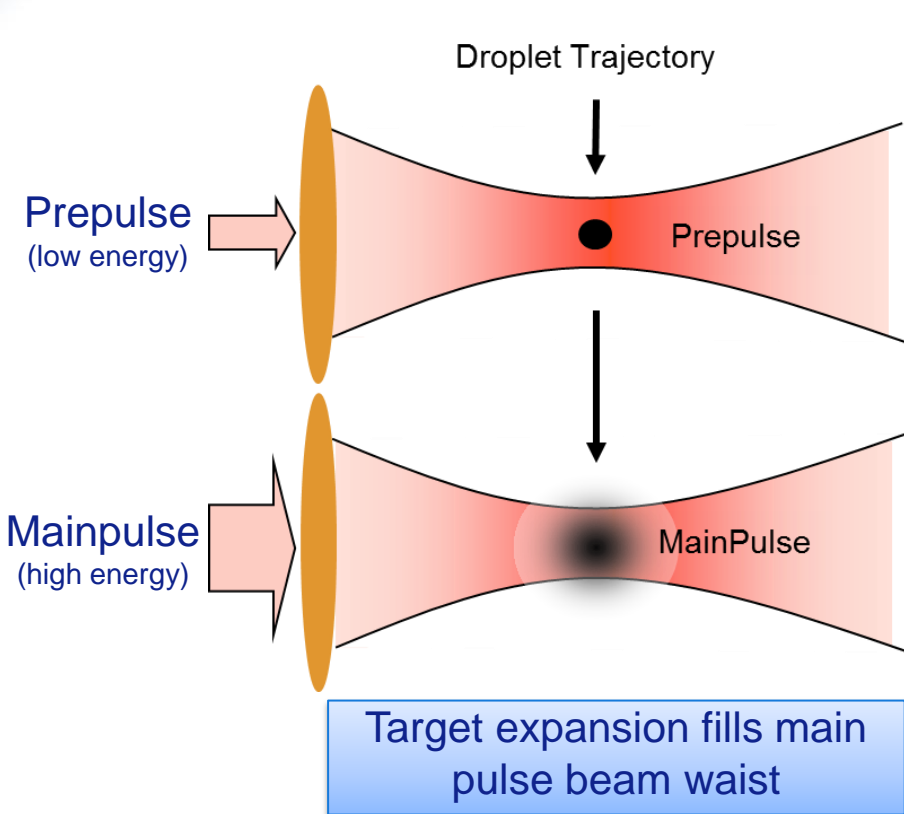
Drive laser: High-power amplifier chain validated



- First HPDL prototypes delivered and integrated with current seed table
 - >30kW total CO₂ power
 - Good beam quality
- Together with new seed system → higher EUV power

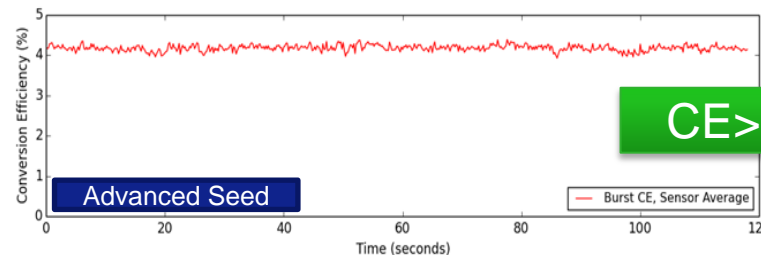
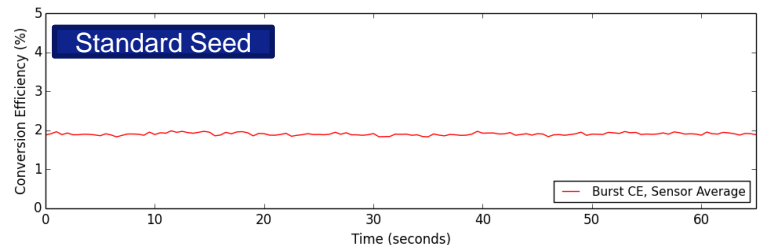
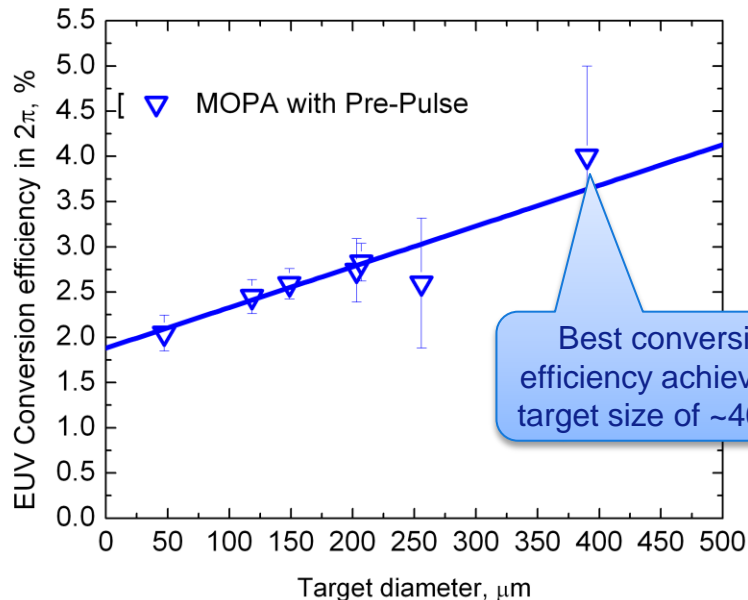


Conversion efficiency: Optimizing pre-pulse to create a more efficient target



High CE demonstrated with optimized pre-pulse

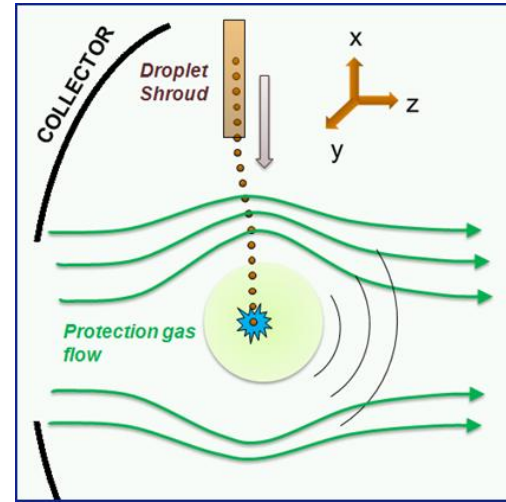
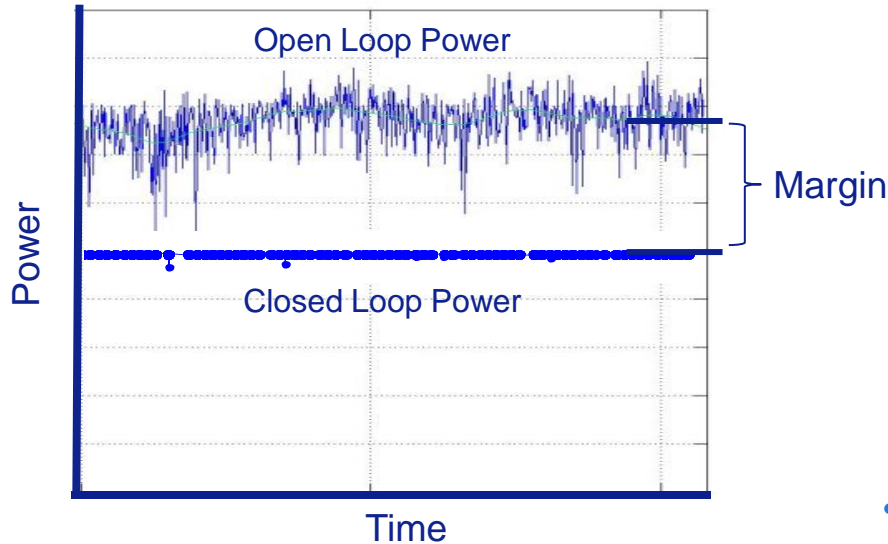
~4% conversion efficiency achieved at high duty cycle



High CE requires control of:

- Target conditioning
- Targeting dynamics
- Focus control

Dose margin: Closing the gap between 'open loop' and stabilized power



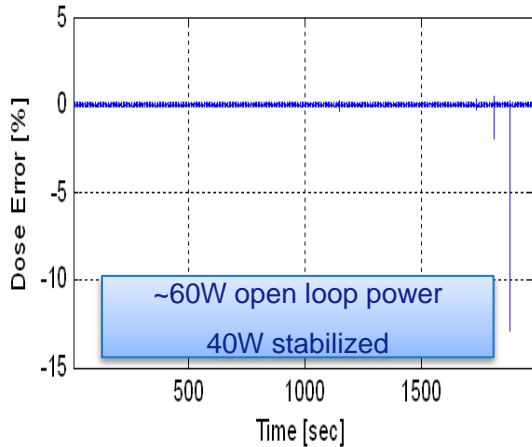
- Margin is the difference between unstabilized open loop power and stabilized closed loop power
- Required for dose control

- Plasma and gas-dynamic forces distort droplet trajectories causing mis-targeting, low CE and energy instability
- Controls that compensate for these forces enable closed loop operation with reduced margin

Advanced controller demonstrated at <10% dose margin

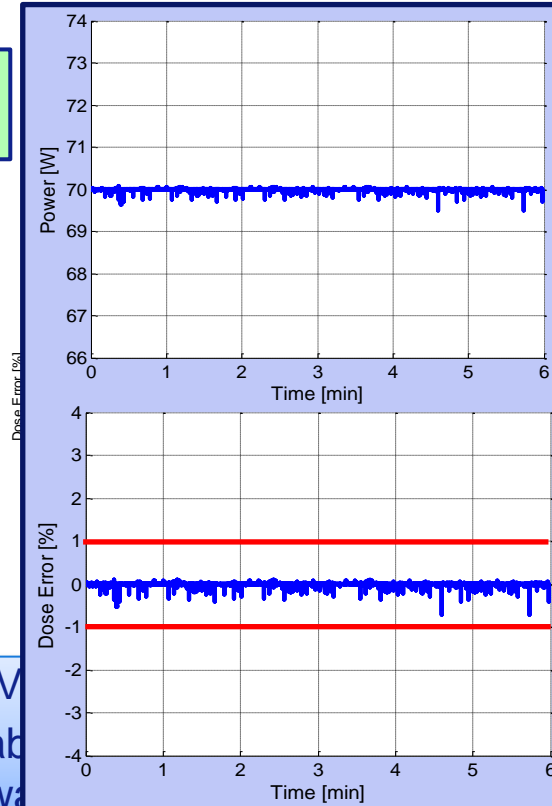
Existing controller: good performance at ~35% margin

Current controller
35% dose margin: In-spec



- Comparison: EUV
- Maximizing available dose control software

EUV Stabilization controller
6% dose margin: In-spec



~75W open loop power
70W stabilized

- Power vs current controller
- margin using advanced

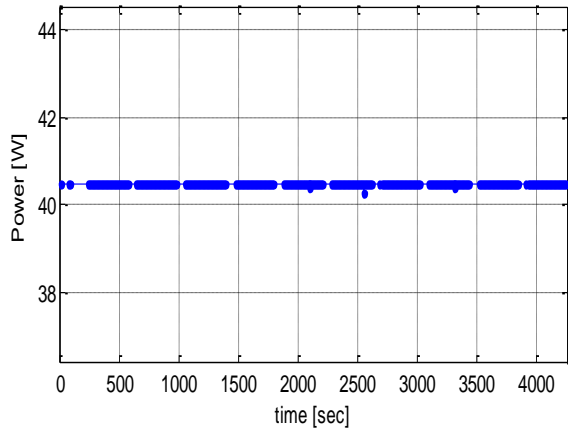
Source power and availability drive productivity

Technology development work is ongoing to improve all aspects

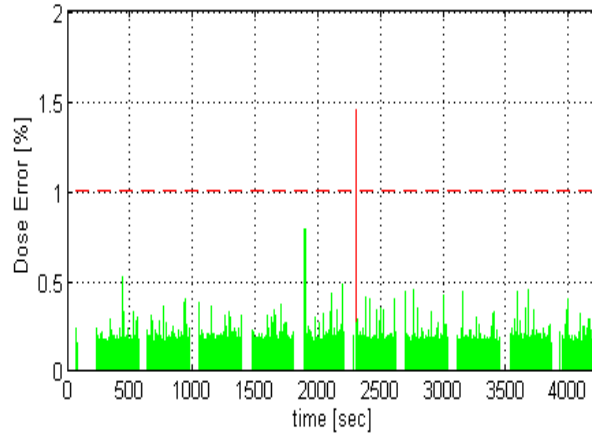


Automation: Stable power, good dose control

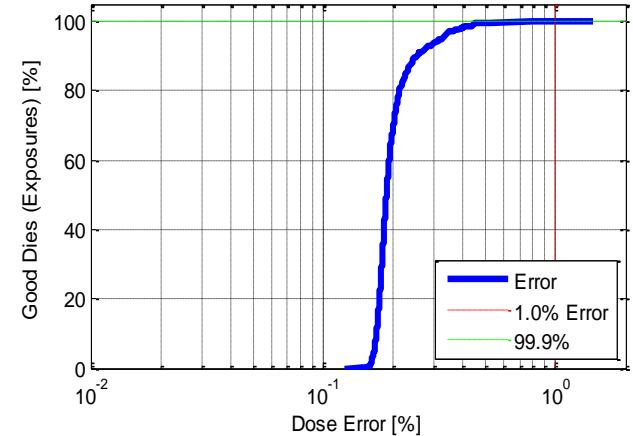
NXE:3300B stand-alone source under full automation for improved availability



~40W stable power
(*x, y, z, t and E loops closed*)



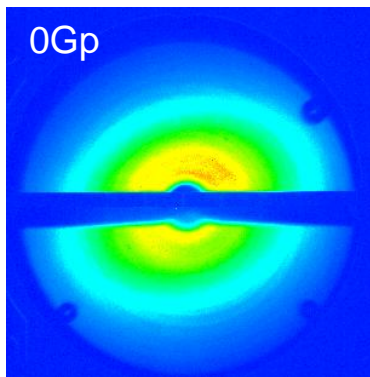
Simulated wafer lot exposures
at 30mJ/cm² resist dose



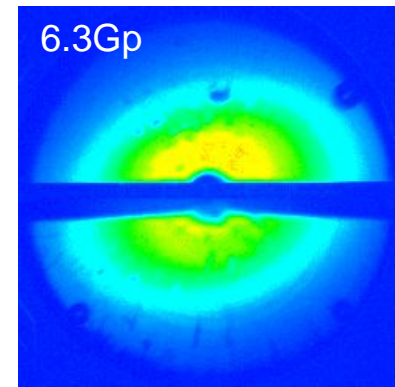
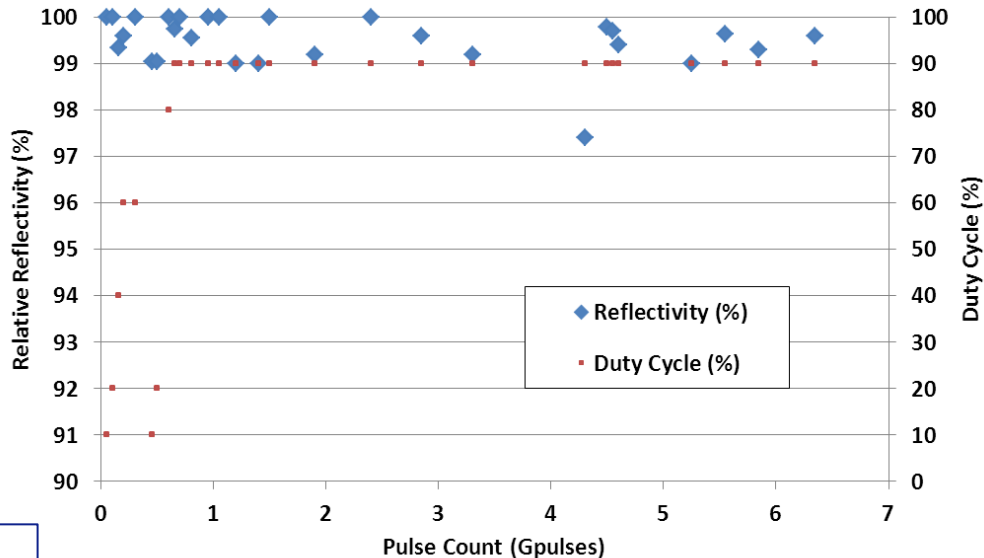
99.9% die yield
(simulated)

No collector reflectivity degradation after >5 Gpulses

NXE:3300B source collector protection test, all control loops closed



0Gp
Far-field EUV image
Initial



6.3Gp
Far-field EUV image
after 6.3Gp

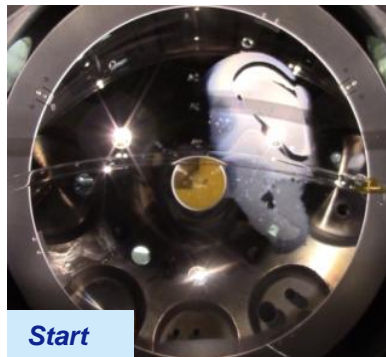
NXE:3300B stand-alone source operating in San Diego

- ~40W, 90% EUV duty cycle
- All control loops closed
- Simulated wafer exposure lots at 25mJ/cm² dose

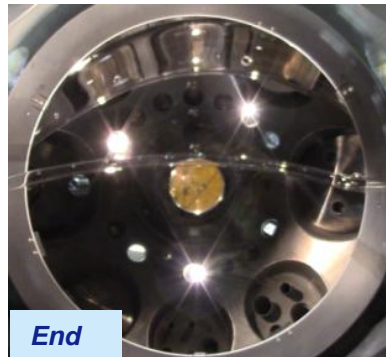
~500 simulated wafer exposures over 6.3Gpulses

In-situ collector cleaning

Effectiveness of product configuration confirmed

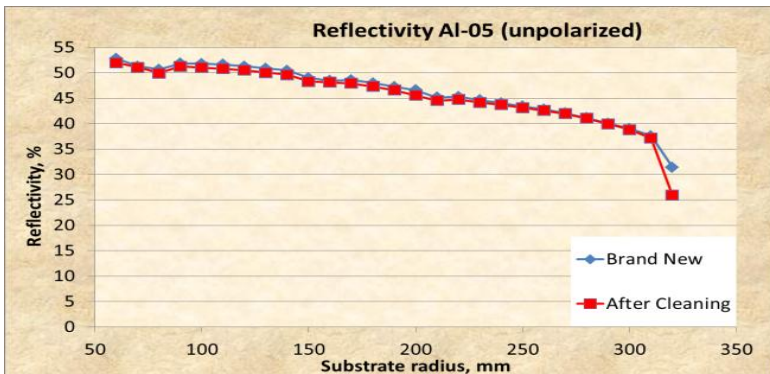
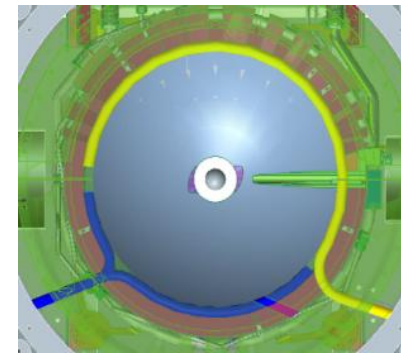


Start

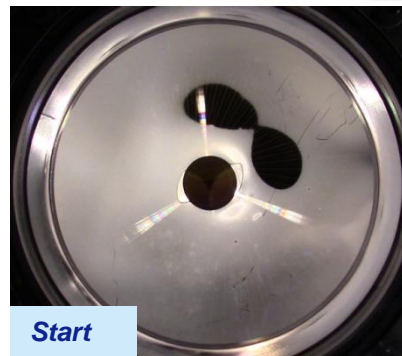


End

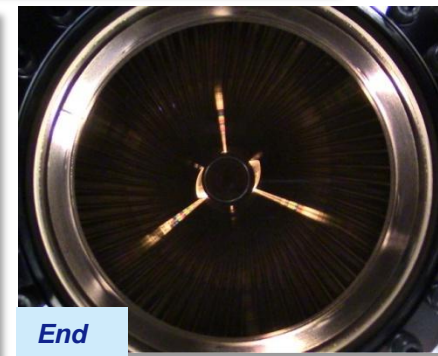
Field collector
cleaned in
NXE:3300 source
vessel test rig



Reflectivity restored within 0.8% of original
Cleaning in off-line MOPA Prepulse development vessel



Start



End

Off-line cleaning using NXE:3300B source
vessel with product configuration hardware



Summary: Source power and availability technology development for improved productivity

Source power

Drive laser: high-power amplifier chain validated

High conversion efficiency of ~4% demonstrated

Dose margin <10% with advanced controller demonstrated

Optical transmission

Source availability

Full automation with good dose control demonstrated

Collector protection: protection & in-situ cleaning validated

Droplet generator reliability & lifetime

Drive laser reliability

ASML

A series of thin, light blue curved lines that sweep from the bottom left towards the center of the page, creating a sense of motion and depth.

CYMER

An **ASML** company