



High-NA EUV lithography enabling Moore's law in the next decade

Jan van Schoot¹, Kars Troost¹, Alberto Pirati¹, Rob van Ballegoij¹, Peter Krabbendam¹, Judon Stoeldraijer¹,
Erik Loopstra¹, Jos Benschop¹, Jo Finders¹, Hans Meiling¹, Eelco van Setten¹
Bernhard Kneer², Bernd Thuering², Winfried Kaiser², Tilmann Heil², Sascha Migura², Jens Timo Neumann²

¹ ASML Veldhoven, The Netherlands

² Carl Zeiss Oberkochen, Germany

15 June 2017, EUVL Workshop, Berkeley

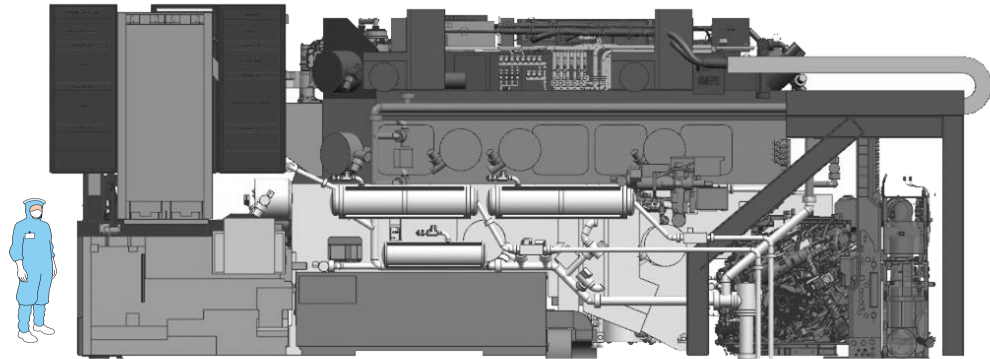
Why high-NA?

Anamorphic Optics

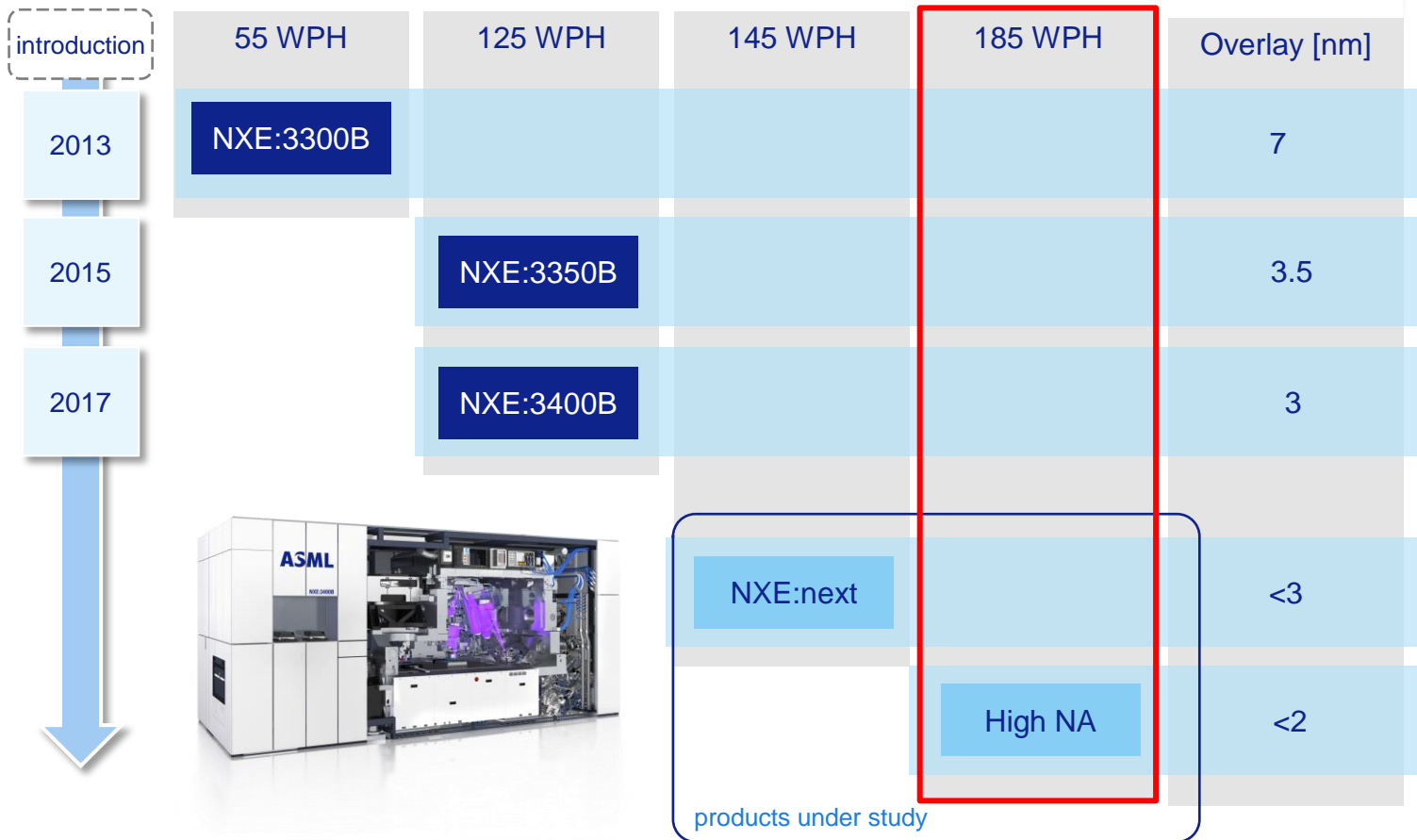
Imaging

System Architecture

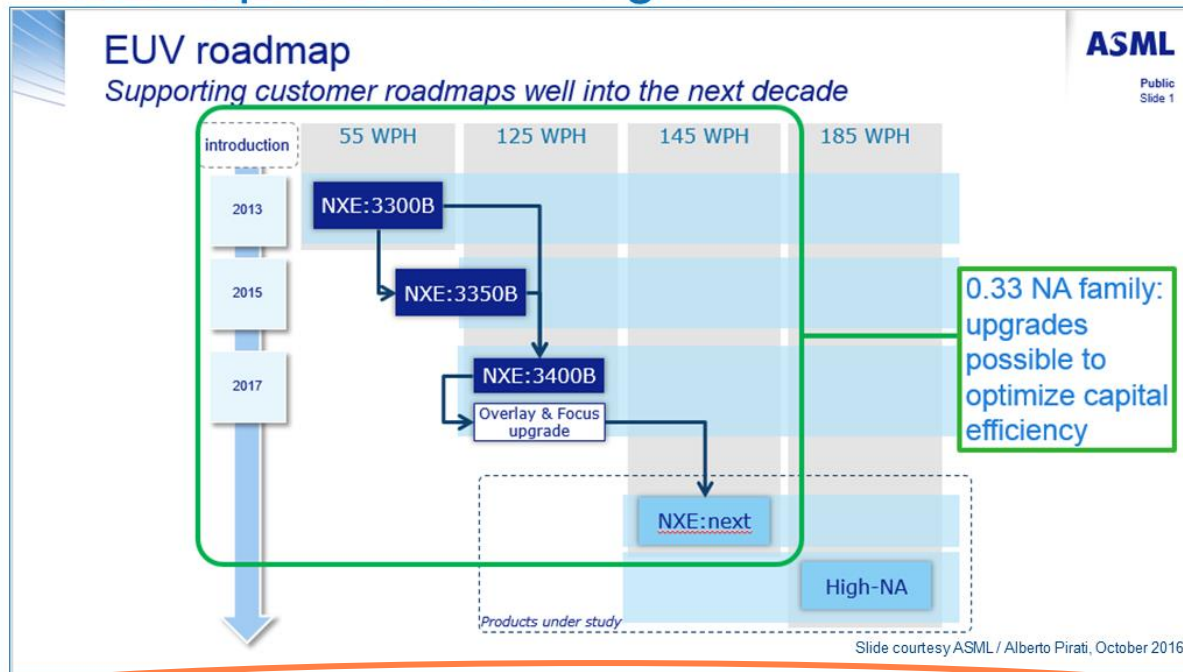
Conclusions



EUV extension roadmap



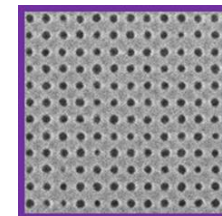
EUV roadmap extension: High NA



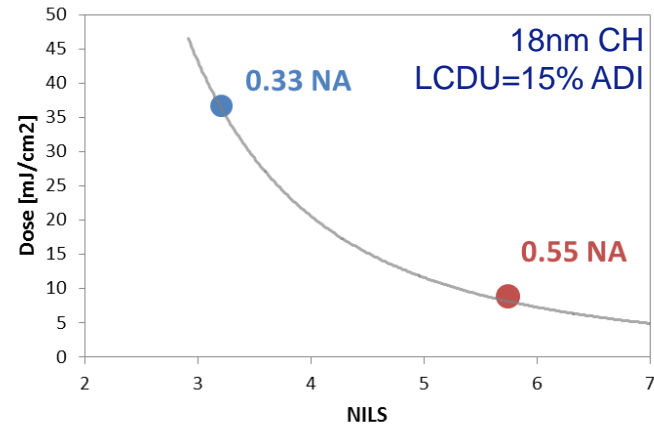
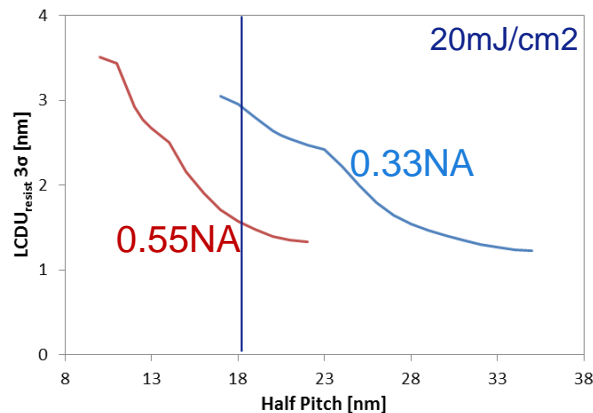
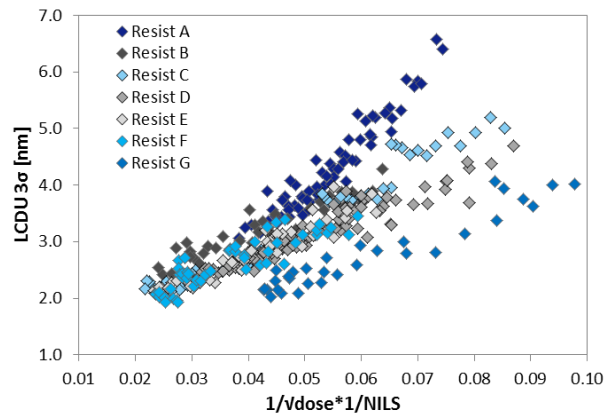
- HF 0.55NA anamorphic optics, higher transmission, fast stages, offer attractive wafer cost / process simplification proposition

Larger NA reduces Local CDU

Due to larger aerial image contrast

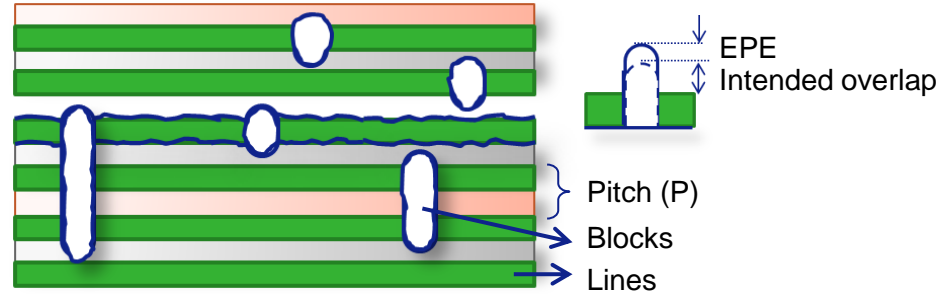


Non-CAR resist, Quasar Illumination



Edge Placement Error determining factor in litho performance

$$EPE_{max} = 'systematics' + 'local' + 'global'$$



$$EPE_{max} = \frac{HR_{OPC}}{2} + \frac{3\sigma_{PBA}}{2} + \frac{6\sigma_{LWR}}{\sqrt{2}} + \sqrt{(3\sigma_{OVL})^2 + \left(\frac{3\sigma_{CDU}}{2}\right)^2}$$

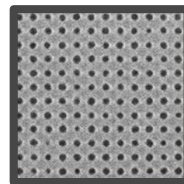
$$\text{With } \sigma_{LWR} = \sqrt{\sigma_{LWR_line}^2 + \sigma_{LCDU_cuts}^2}$$

$$\text{and } \sigma_{CDU} = \sqrt{\sigma_{CDU_lines}^2 + \sigma_{CDU_cuts}^2}$$

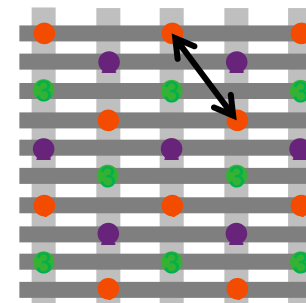
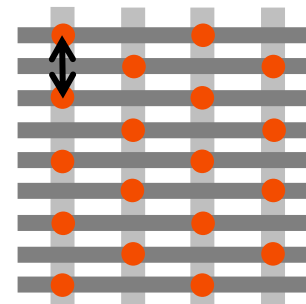
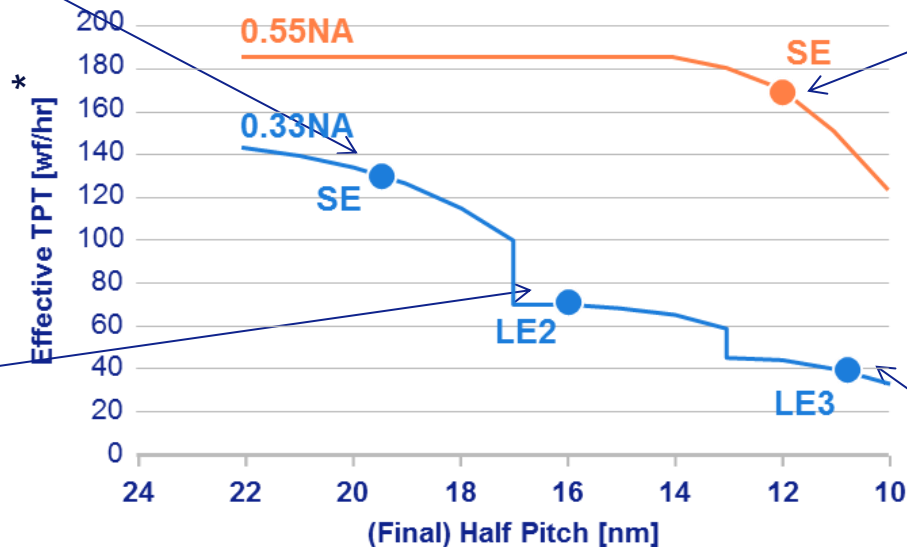
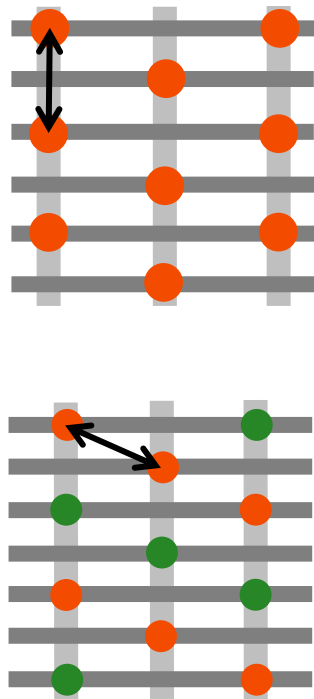
Bring system at EPE specifications by
adapting the dose → Throughput

Larger NA results in higher effective throughput

NA limits dose and # of LE steps



Quasar Illumination



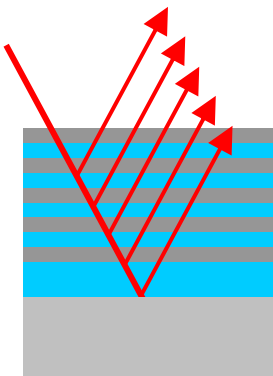
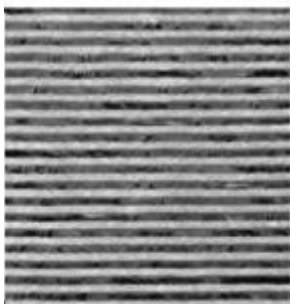
* Effective throughput = throughput / # LE steps

Anamorphic Optics

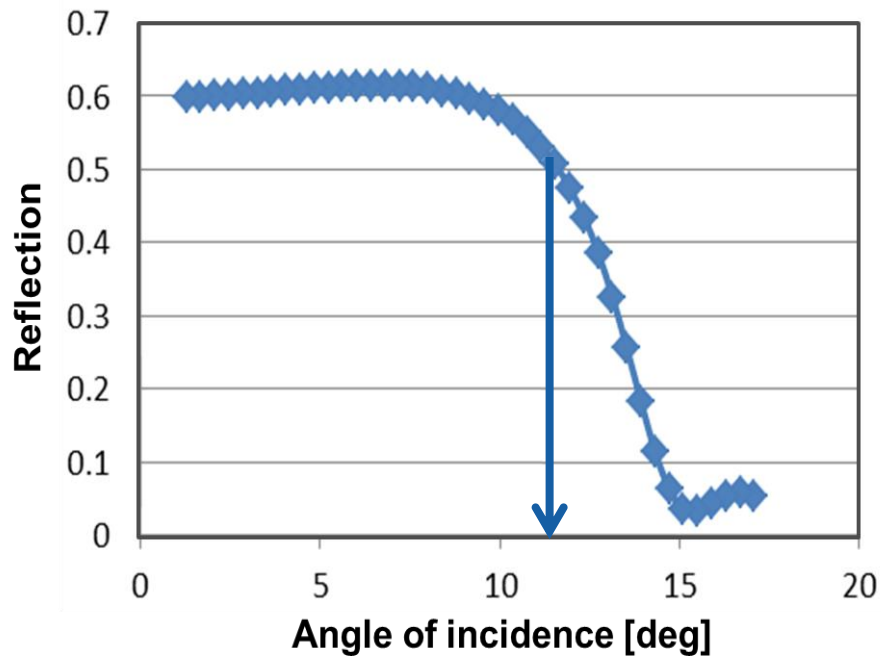
EUV: it's all about the angle

High-NA comes with large angles

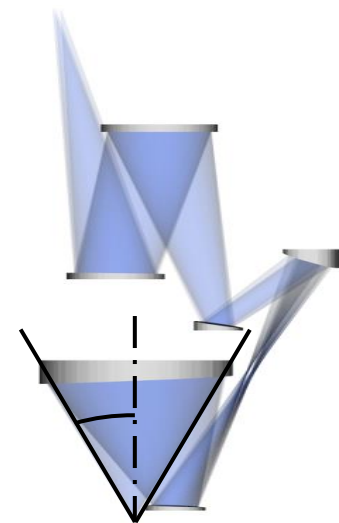
MoSi Multilayer



ML reflection



NA=0.55

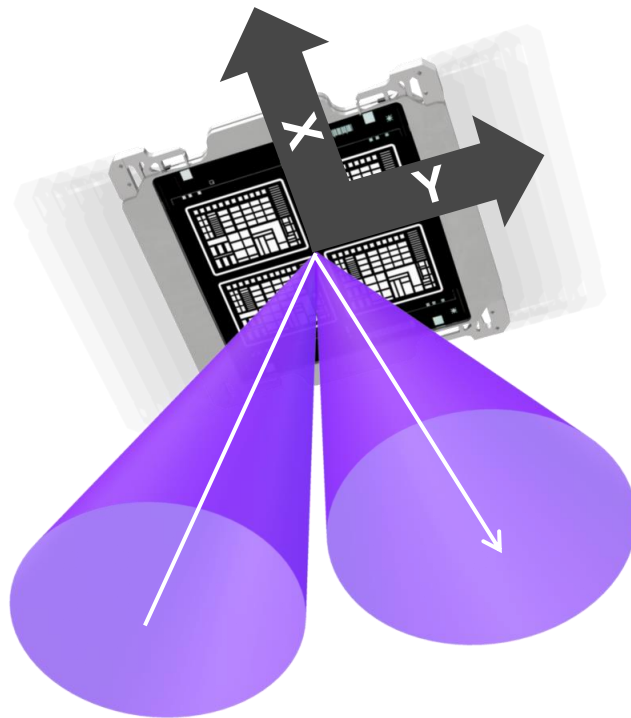
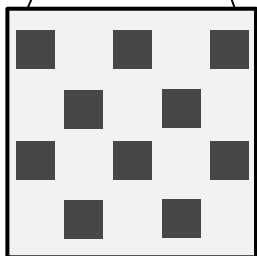
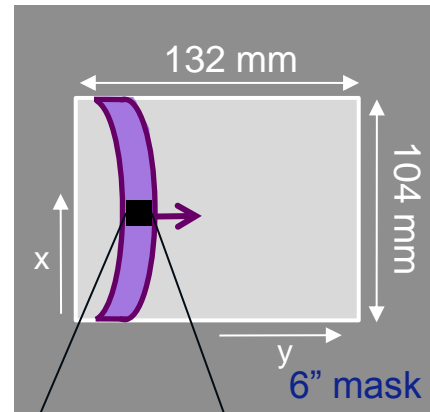


Light cones at the mask for a 0.33NA Scanner

Enabling a solution with 26 mm slit on 6" masks

Reticle

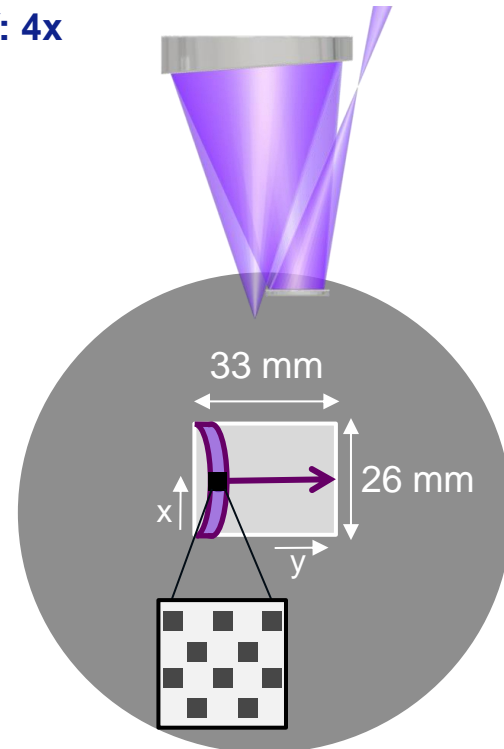
Reticle layout compatible with today 6" mask production



Projection with 0.33 NA

Mag X: 4x

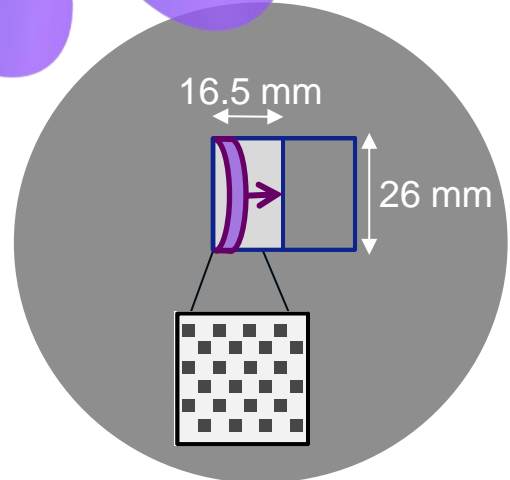
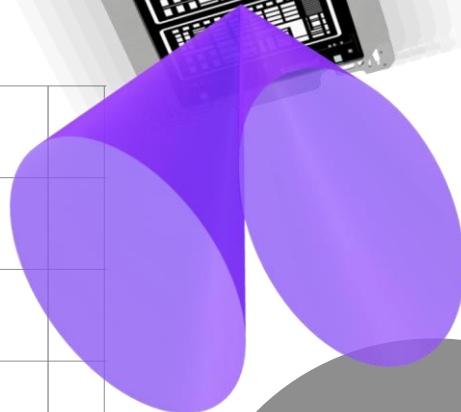
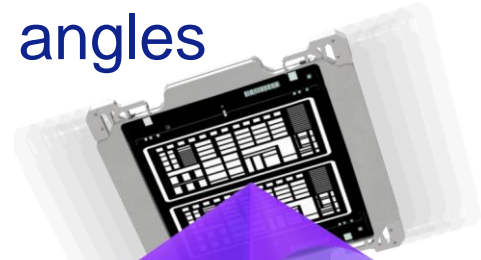
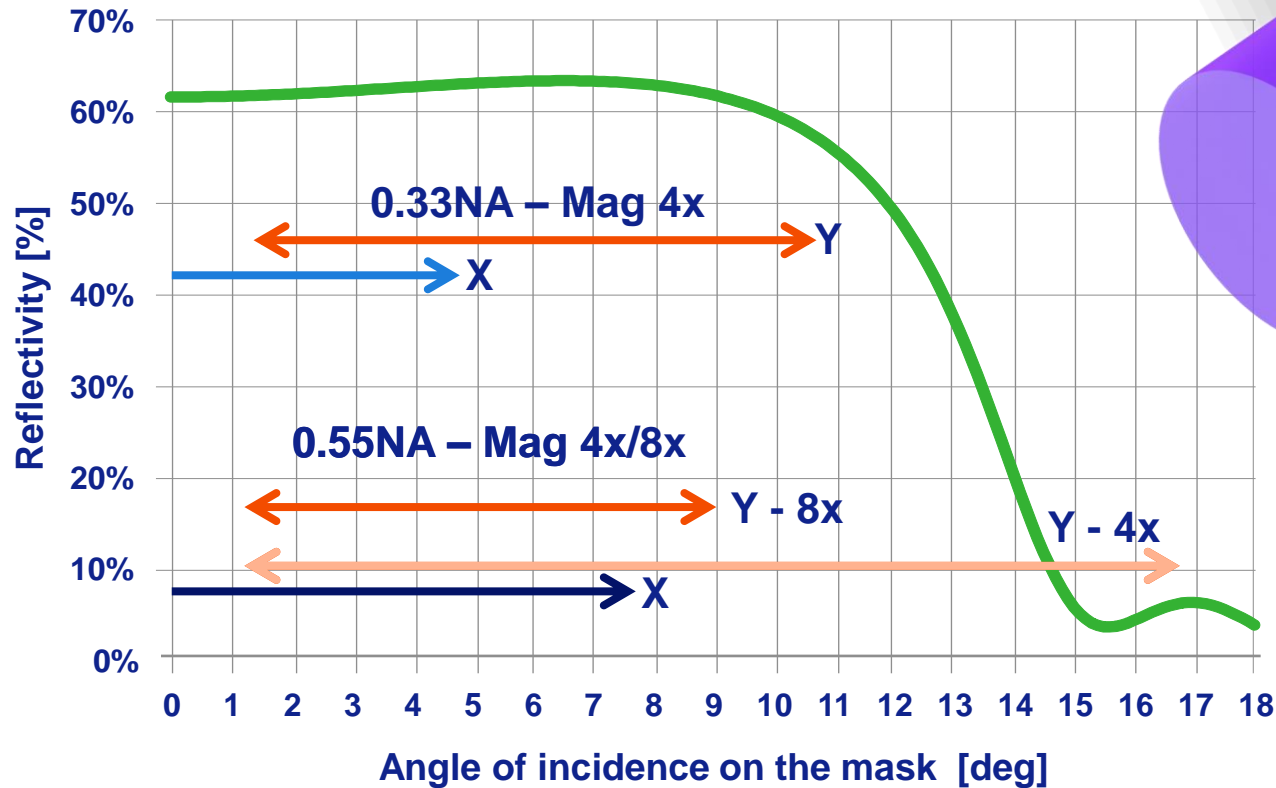
Mag Y: 4x



Anamorphic High-NA EUV reduces the angles

Enabling a solution with 26 mm slit on 6" masks

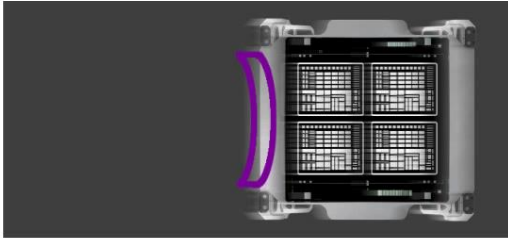
Multilayer Reflectivity



Wafer

High-NA anamorphic Half Field concept

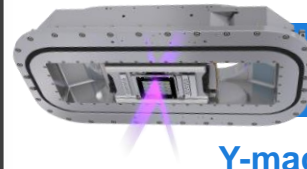
Faster stages required to obtain high productivity half-field scanner



Projection: 0.33 NA

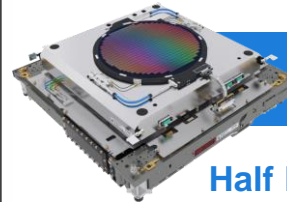
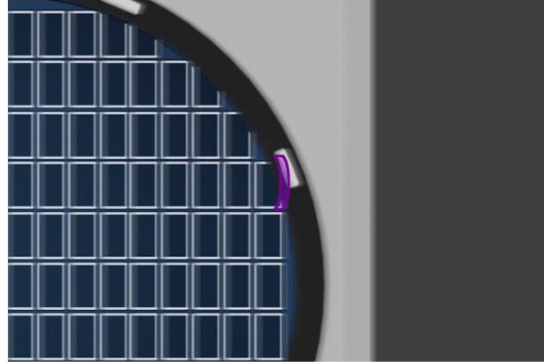
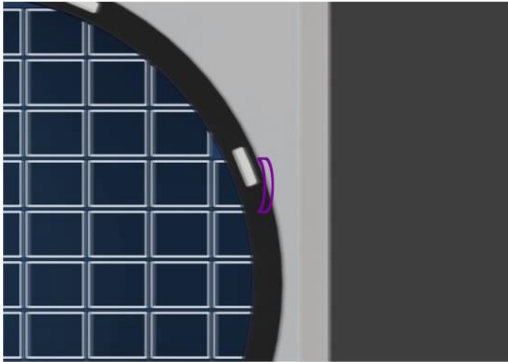


Projection: > 0.5 NA



Acceleration of mask stage ~4x

Y-magnification 4x → 8x:
2x wafer acceleration results
in 4x mask acceleration

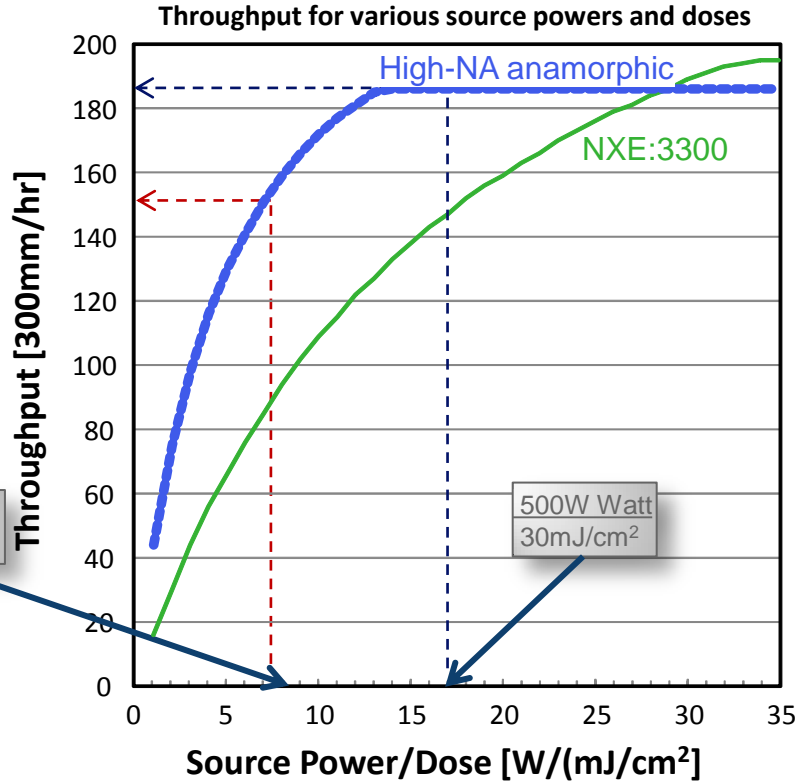


Acceleration of wafer stage ~2x

Half Field yields 2x more fields:
2x wafer stage acceleration maintains
overhead while going
to twice number of scans

High-NA Field and Mask Size productivity

Throughput of >185wph with anamorphic HF



WS, RS current performance

WS 2x, RS 4x

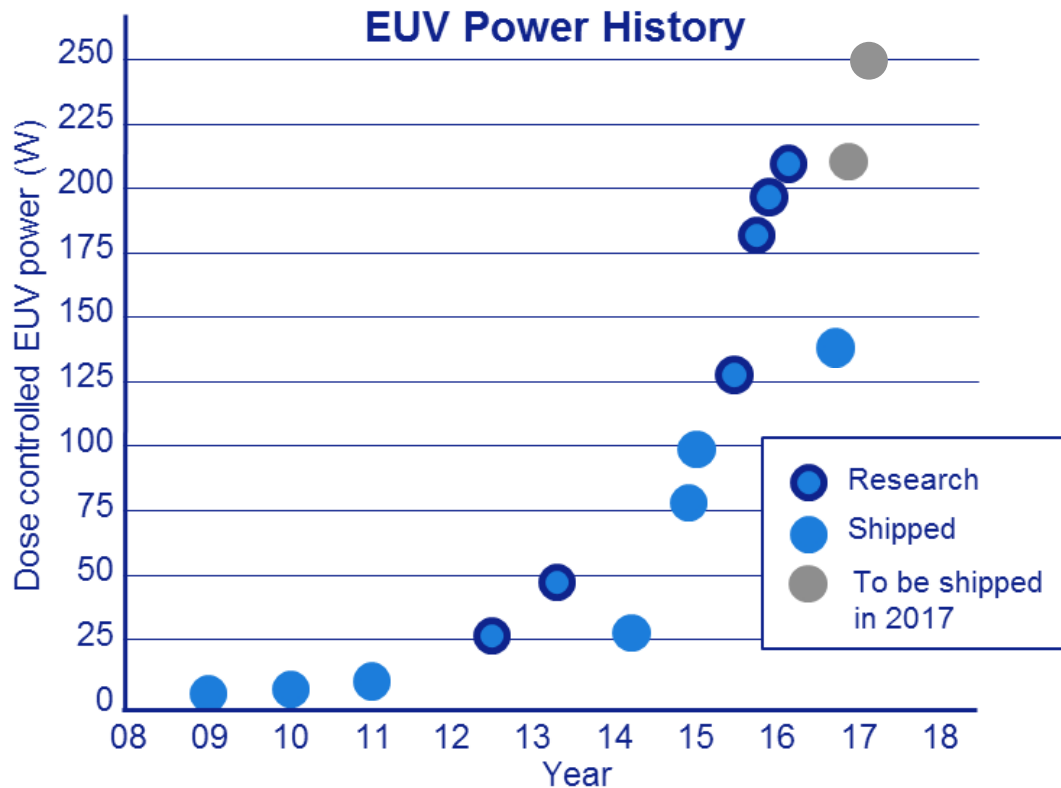


HF

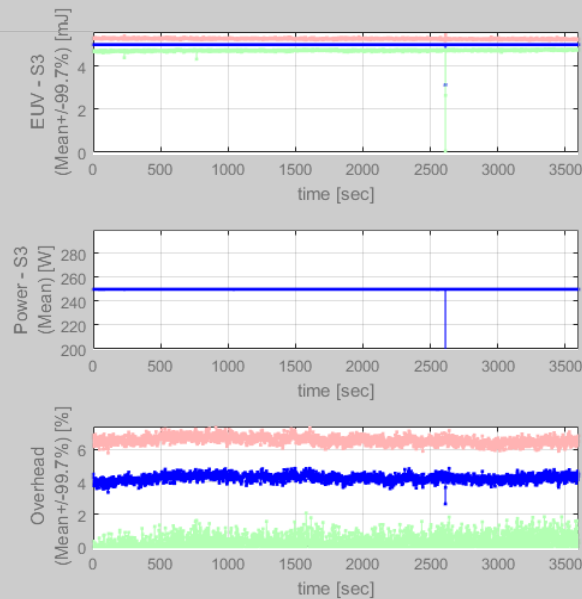


High-NA Half Field scanner
needs 500W for
150wph at 60mJ/cm²

Source power: 250W demonstrated, 10x improvement in five years **ASML**



250W with dose in specifications obtained on development source

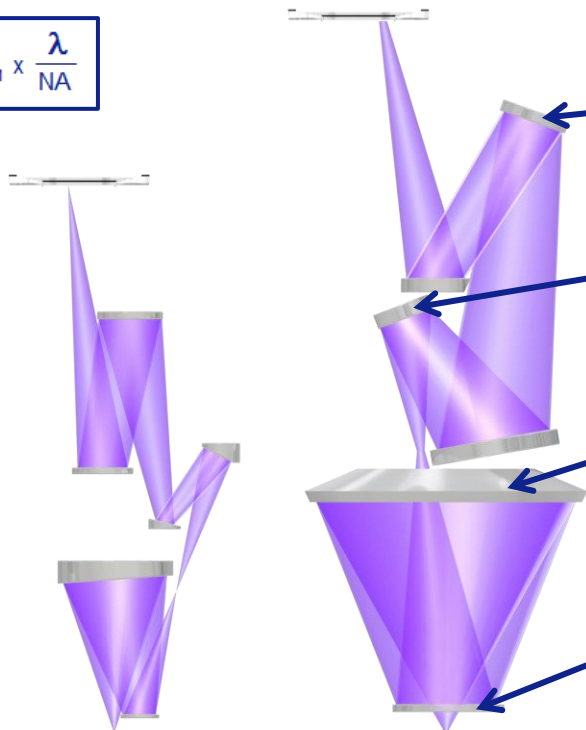


High-NA optics design available

Larger elements with tighter specifications

$$\text{Resolution} = k_1 \times \frac{\lambda}{\text{NA}}$$

Reticle level



Extreme aspheres enabling further improved wavefront / imaging performance

Tight surface specifications enabling low straylight / high contrast imaging

Big last mirror driven by High-NA

Obscuration enables higher optics transmission
→ Potential of up to 2x vs 3300

Wafer level

NA 0.33

NA >0.5

Design examples

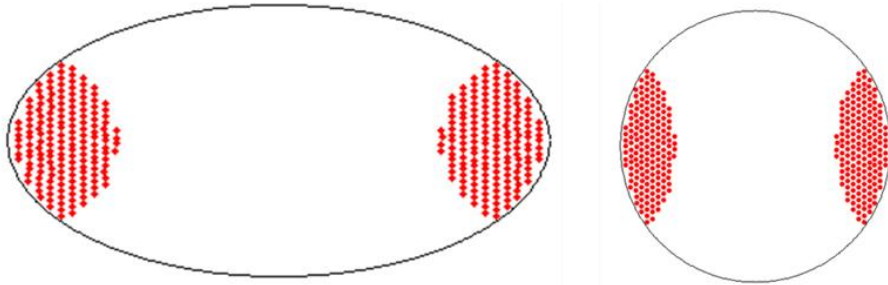
High-NA flexible illuminator

Principle NXE:3300/3400 illuminator can be reused

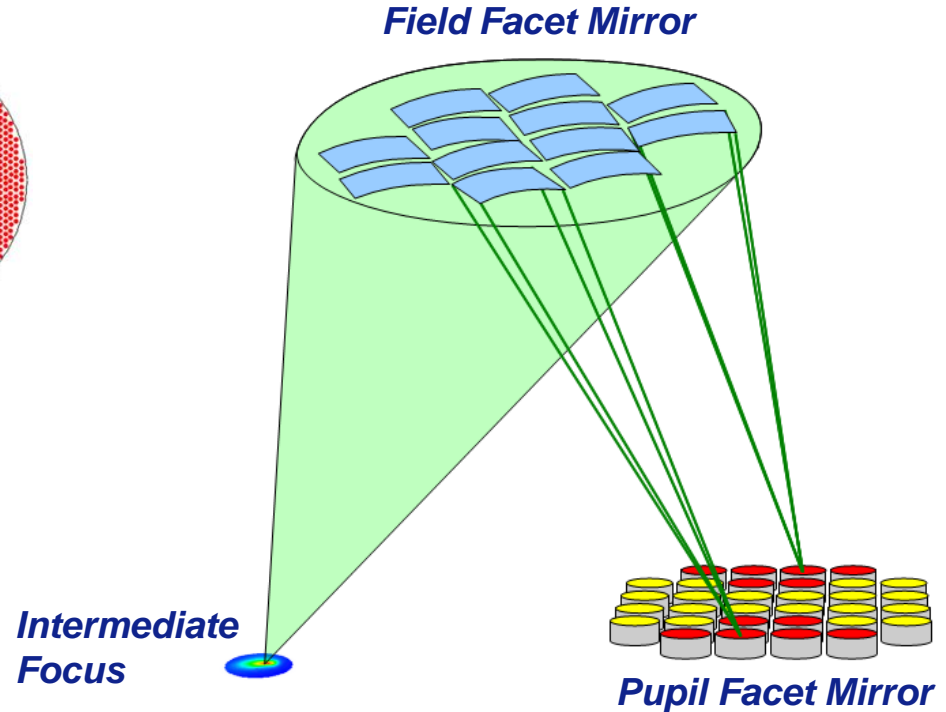


ASML

Public
Slide 16
15 June 2017



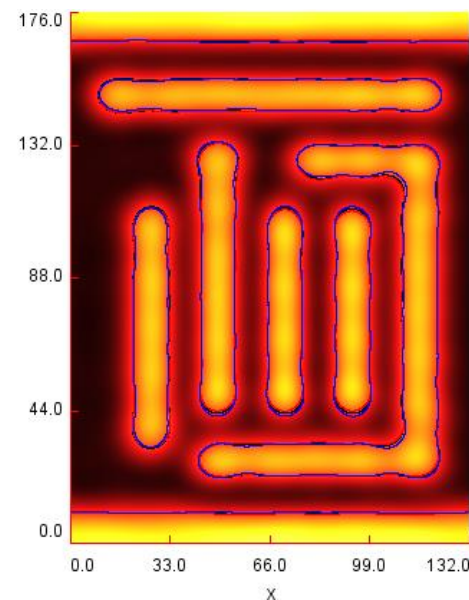
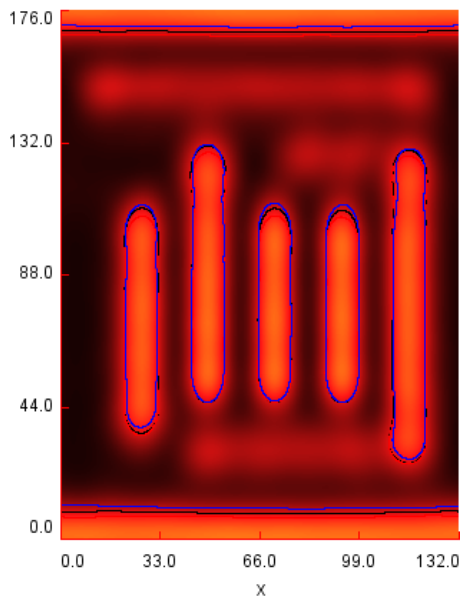
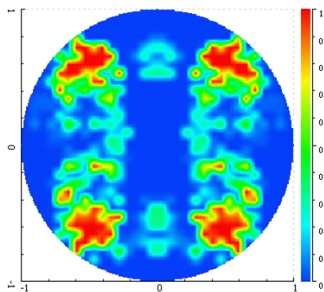
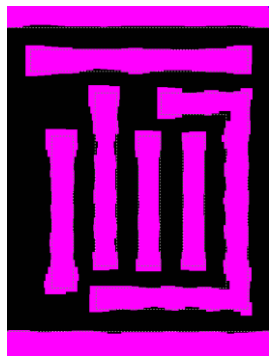
Illuminator elliptical pupil is projected in a circular pupil at the wafer



Imaging verification of the new Half Field concept

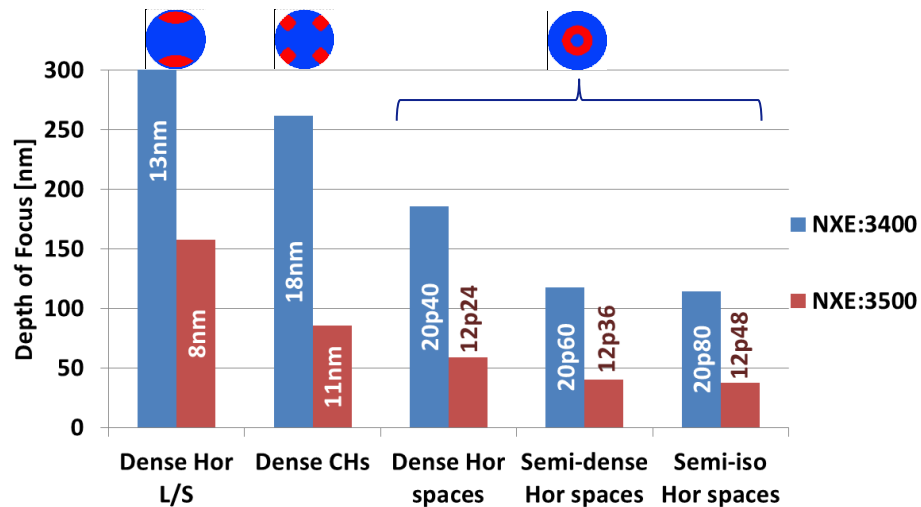
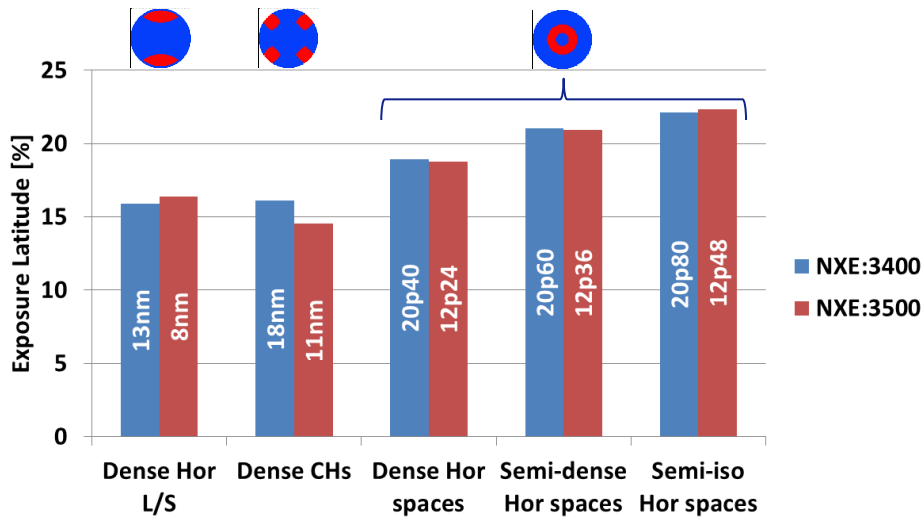
Logic N5 clip Metal-1, 11nm lines, SMO is done at 8x

Aerial Image Intensity in Hyperlith



Note: pictures at same scale,
smaller mask reflection is
also visible

Imaging evaluation of key lithographic structures: comparable performance as 0.33 NA at ~40% lower resolution



Comparable Exposure Latitude at 40% lower resolution

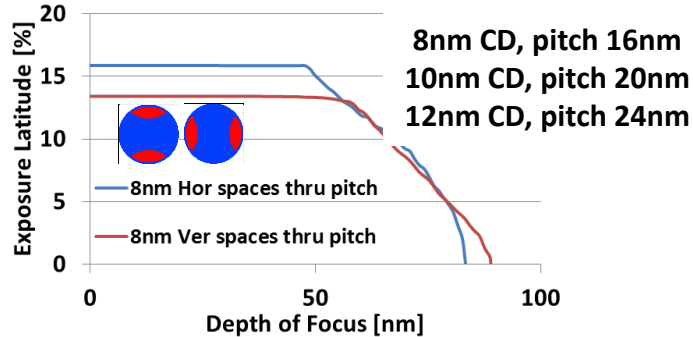
DOF at equivalent k_1 factor
Follows NA scaling \rightarrow

$$\left(\frac{0.33}{0.55}\right)^2$$

Simulations based on high-NA lens Jones pupil; mask 3D effects included

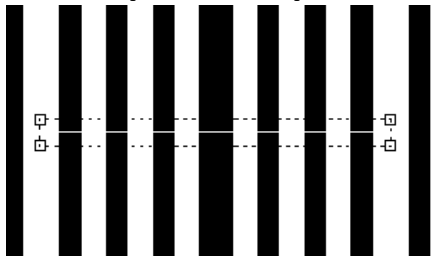
Good overlapping process window for customer relevant structures

8nm spaces through pitch

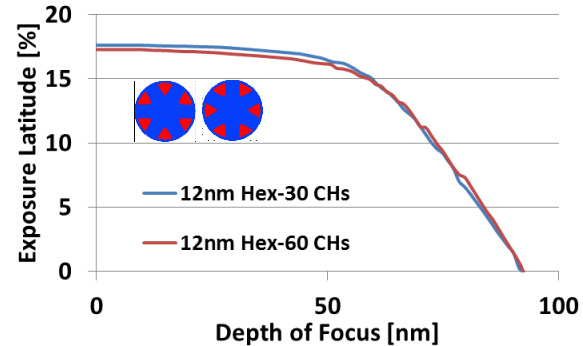


DoF @ 10% EL (H,V) = 68 / 56nm

Multi-pitch L/S pattern

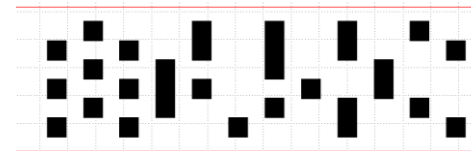


12nm staggered CHs



DoF @ 10% EL = 73nm

Logic cutmask

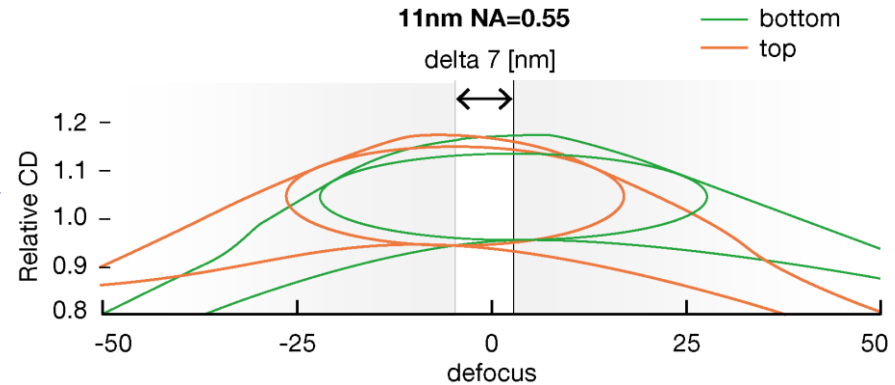
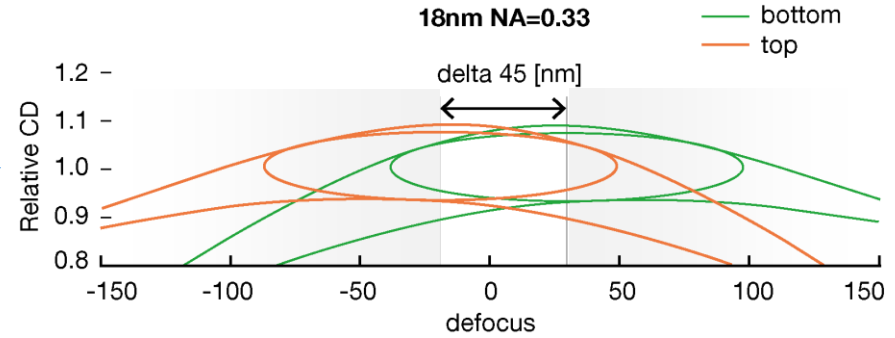
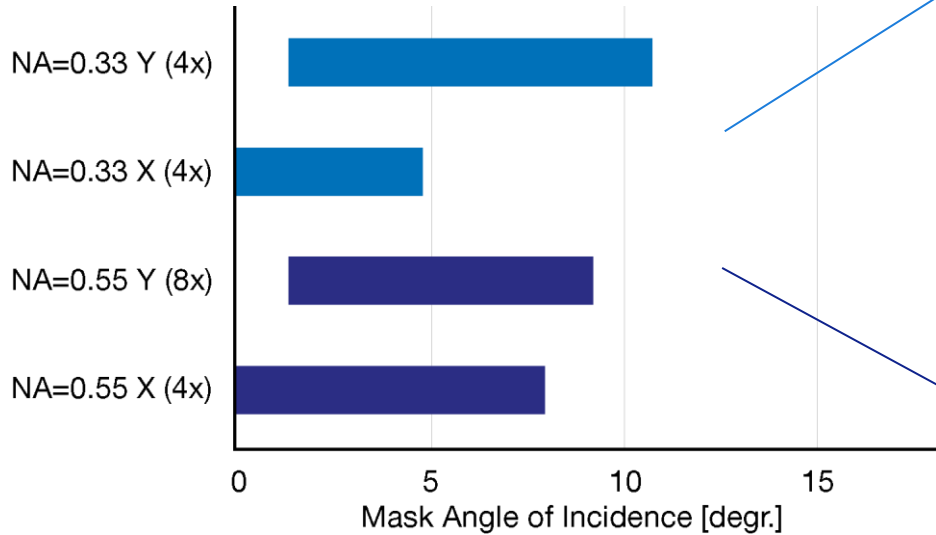


High-NA system has smaller M3D effects than 0.33NA

Smaller mask angles of incidence due to anamorphic system



Two-bar trenches are a canary for M3D effects



*L. de Winter, Understanding the Litho-impact of Phase due to 3D Mask-Effects when using off-axis illumination, EMLC 2015

System Architecture

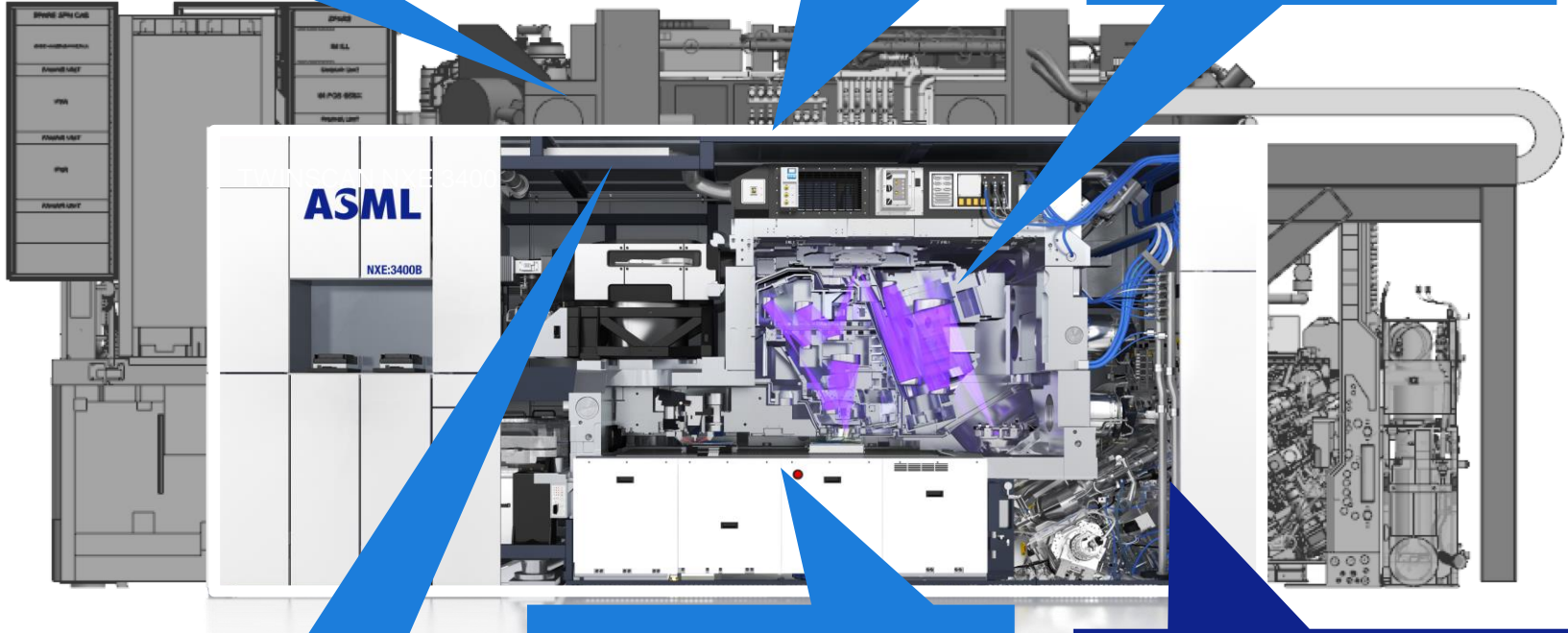
High-NA system architecture available

Improved metrology
2~3x improvement in overlay/focus

Mask Stage
4x increase in acceleration

Lens & illuminator

- NA 0.55 for sub-10nm resolution
- High transmission



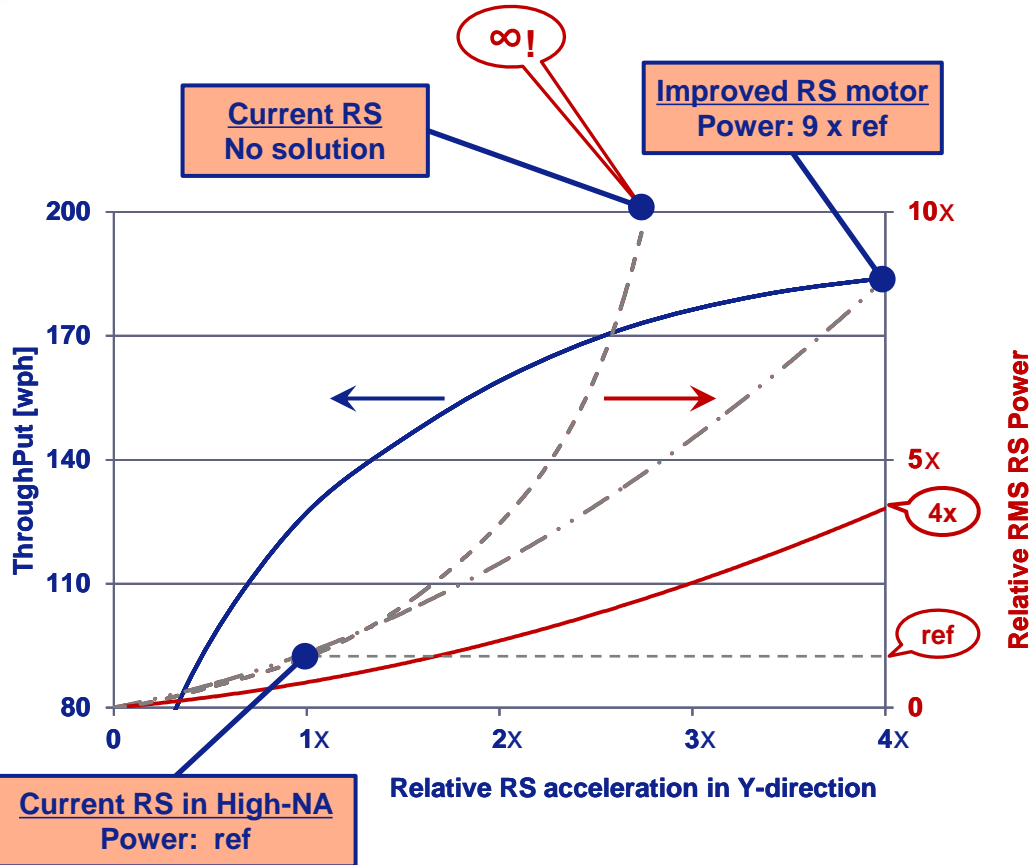
New Frames
Improved thermal and dynamic control with larger optics

Wafer Stage
2x increase in acceleration

Source
Compatible with 0.33 NA sources, power improvements opportunities over time

High-NA Mask Stage solution for increased acceleration

Improved motor technology & different -light weight- architecture



$$\text{Power} \sim I^2 \cdot R$$

$$= k \cdot (\text{acc} \cdot \text{mass})^2 \cdot R_{\text{motor}}$$

Limiting increasing power by:

- Improved motor technology (k, R)
- Reduce mass

Further Optimizing power consumption:

- New stage architecture with lower mass

Reticle stage acceleration

4 x 3400 acceleration

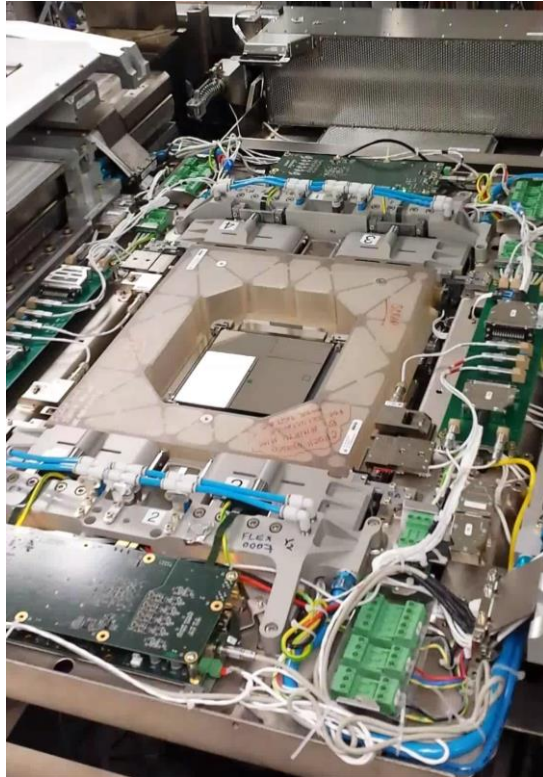
3400 reticle stage



High-NA reticle stage



Mask stage short-stroke motor: demonstrated improved accuracy at high acceleration

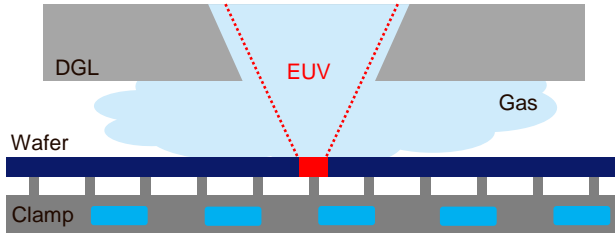


	Measured	Required
Actuators force (N)	ref	1.2x
Positioning accuracy (MA-Y, nm)	0.37	≤ 1.1

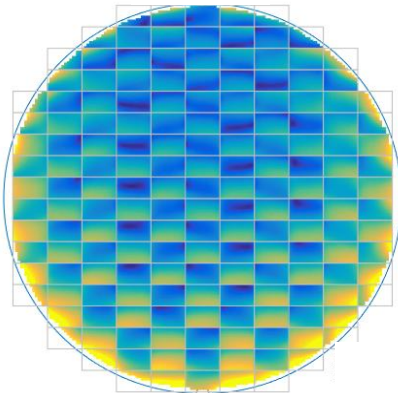
Cooling Hood as Wafer Heating Solution

Extract heat from wafer using a cold body & gas pressure outside exposed area

NXE:3500 without Top Cooler

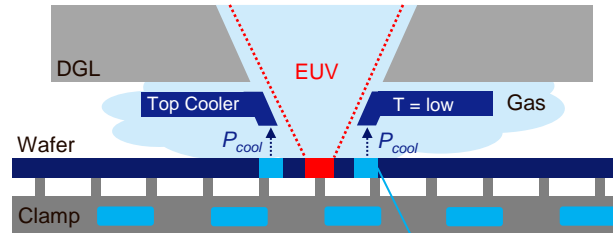


Modelled raw distortions

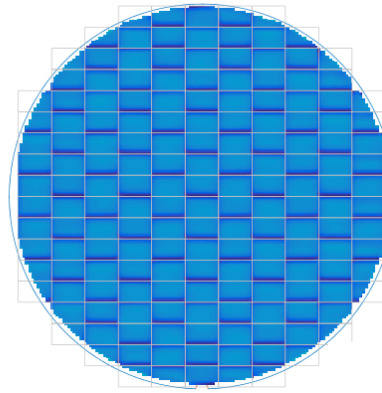


Include
Wafer Top
Cooling

NXE:3500 with Top Cooler ("Cooling Hood")



Modelled raw distortions



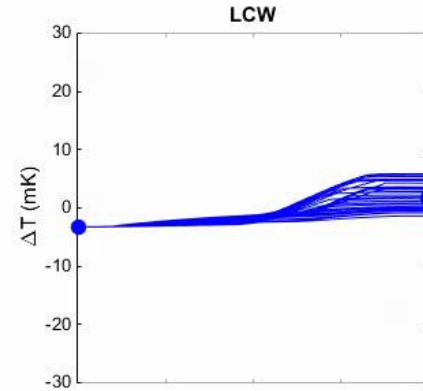
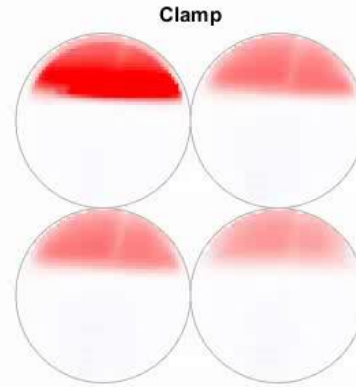
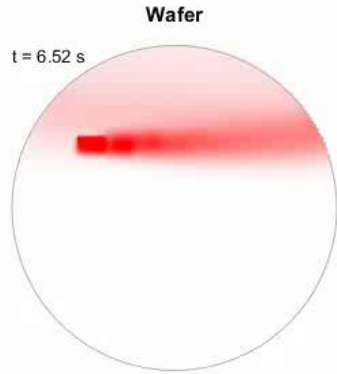
$$P_{cool} = HTC(p, z) \cdot A \cdot dT$$

P_{cool} = Cooling power [W]
 A = Top cooler area [m²]
 dT = $T_{wafer} - T_{hood}$ [K]
 HTC = heat transfer coefficient
 [W/(m²·K)]

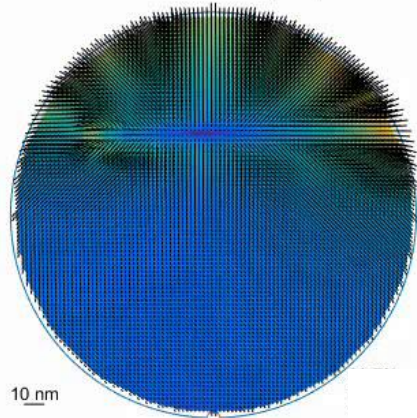
Top Cooler working principle:

- Heat from wafer to cold body via gas
- Match P_{cool} with exposure load,
- Switch cooling on/off between scans
→ gas pressure switching

Wafer Heating NXE:3500 without cooler

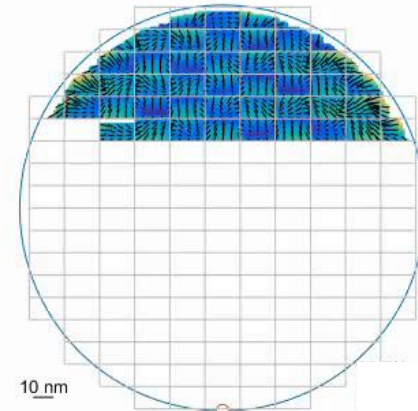


-300 .. 300 mK
Wafer deformation (F2N)

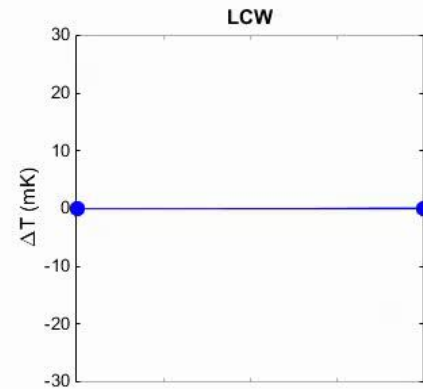
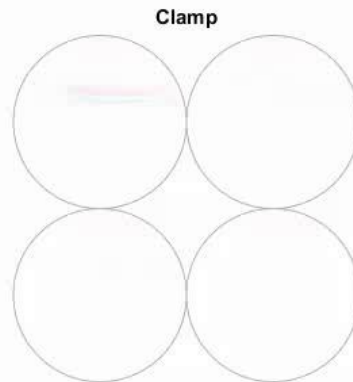
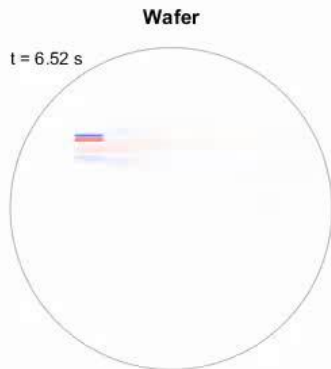


-30 .. 30 mK

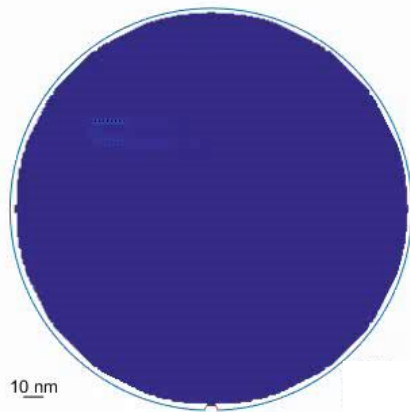
Overlay (F2N)



Wafer Heating NXE:3500 with cooler

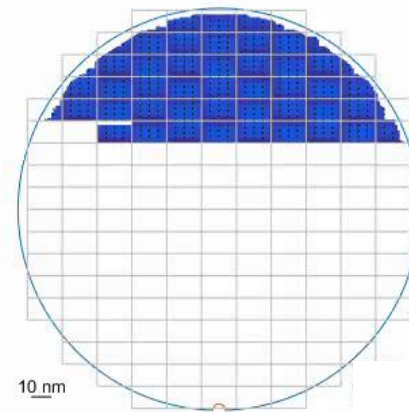


-300 .. 300 mK
Wafer deformation (F2N)



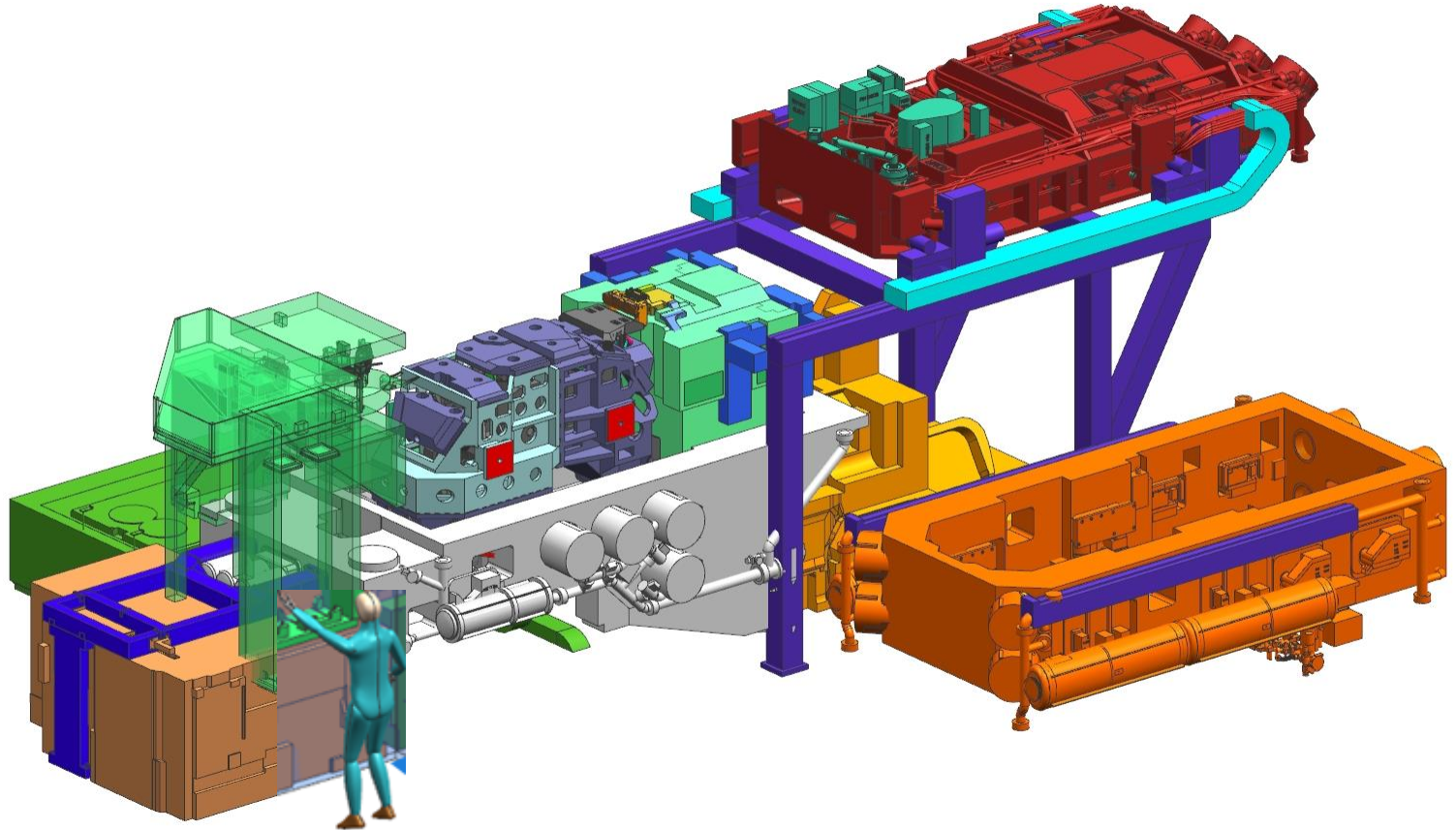
-30 .. 30 mK

Overlay (F2N)



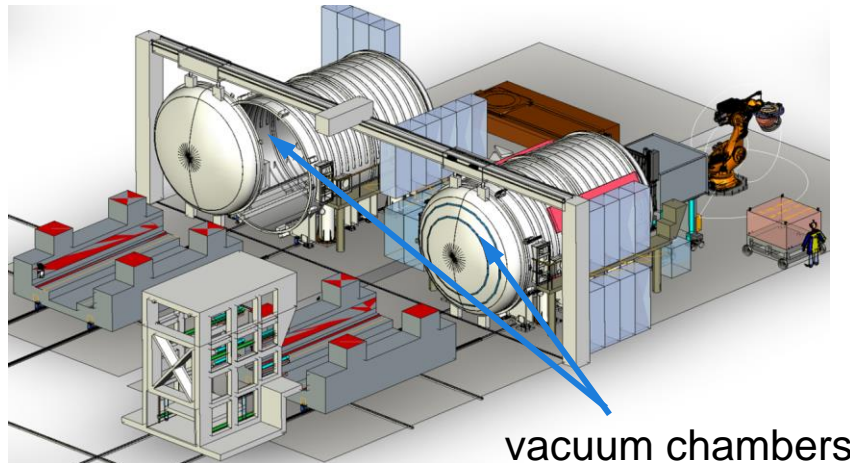
Mechanical layout

Modularity key for manufacturing, shipment and service



High-NA surface metrology

- ❑ Accuracy of mirror surface metrology is key for imaging quality
- ❑ High-NA wavefront needs improvement of factor $\sim 2x$ compared to 3300 \rightarrow 2x better measurement accuracy required
- ❑ For larger mirrors: Diameter of mirrors about doubled

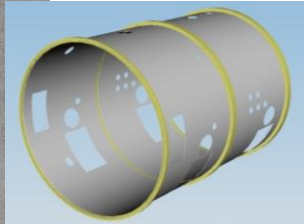


vacuum chambers



Non-design but leadtime critical components ordered

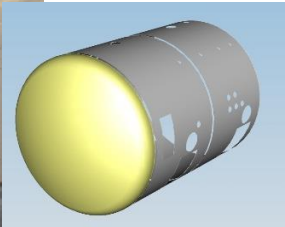
Chamber flanges





Non-design but leadtime critical components ordered

Chamber doors



New grinding technology and machinery



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Facility construction Zeiss in Oberkochen started



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- **High-NA extends Moore's Law into the next decade**
 - Larger contrast of High-NA helps mitigating LCDU
 - New anamorphic concept enables good imaging with existing mask infrastructure resulting in a Half Field image
 - New stages technologies and high transmission enable throughput ~185WpH
 - We are closing the feasibility, optics in design phase, first HW in place

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Thank you for your attention



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